

## Faculty Personal Information



<b>Designation</b>	:	Associate Professor
<b>Years of Experience</b>	:	13 Years
<b>Email Id</b>	:	<a href="mailto:s.karunakarreddy@aceec.ac.in">s.karunakarreddy@aceec.ac.in</a> karunakarreddy_s@rediffmail.com
<b>Employment Status</b>	:	Full Time - Ratified by JNTUH
<b>Areas of Research</b>	:	Antenna Design and Radar Signal Processing
<b>UG Degree</b>	:	B. Tech (Electronics and Communication Engineering)
<b>PG Degree</b>	:	M. Tech(Digital Systems and Computer Electronics)
<b>Ph.D</b>	:	--

### **Subjects Taught:**

1. Electronic Devices and Circuits
2. C and Data Structures
3. Network Analysis
4. Signals and Systems
5. Probability theory and Stochastic Process
6. Switching Theory and Logic Design
7. Electromagnetic Waves and Transmission Lines
8. Linear IC Applications
9. Analog Communications
10. Antennas and Wave Propagation
11. Digital Signal Processing
12. Microwave Engineering
13. Radar Systems

### **No. of publications in International journals:**

1. B.Ramesh Naidu, N.Bhojanna, S.Karunakar Reddy, P.Sravani “Design of Optimized Heterogeneous Spectrometers for Radio Astronomy Applications using Xilinx FPGAs” International Journal of Scientific Engineering and Technology Research”, ISSN: 2319-8885, Vol. 3, Issue 31, Oct 2014, pp.6210-6215.
2. Jayakrishna.P, S Karunakar Reddy, Srinivas.K “Design of Programmable BIST and Repair Analysis using Selectable Redundancy for Embedded SRAM”, International Journal of Inovative Research and Studies, ISSN: 2319-9725.
3. D.Dayakara Reddy, S.Karunakar Reddy, “ FPGA Implementation of QAM Transmitter and Receiver”, International Journal of Engineering Research and Applications (IJERA) ISSN: 2248-9622 www.ijera.com Vol. 3, Issue 1, January-February 2013, pp.048-051.
4. G.Sai Prasanna, S.Karunakar Reddy “Implementation of VOIP Communication on Embedded Systems”, International Journal of Research in Computer and Communication technology, IJRCCT, ISSN 2278-5841, Vol 2, Issue 1, January 2013.
5. T. Nagarjuna Reddy, S. Karunakar Reddy, “ PCA Based Palm-Print Recognition System Using DM3730” International Journal of Advanced Research in Computer Science and Electronics Engineering (IJARCSEE) Volume 1, Issue 9, November 2012, ISSN: 2277 – 9043.
6. C.Omshanker, K. Satish Babu, S. Karunakar Reddy “Fast Design and Implementation of Data Encryption Algorithm on Spartan 3E”, International Conference on Information Technology, Electronics and Communications (ICITEC – 2012) Hyderabad, India, July 14 – 15, 2012.
7. K.Satish Babu, Y N Supraja , S.Karunakar Reddy “A Novel Approach Based Image Tracing”, (IJCSIT) International Journal of Computer Science and Information Technologies, Vol. 3 (3) , 2012, 4392- 4395, ISSN:0975-9646.
8. K.Srinivas, S.Karunakar Reddy, M.Madan Gopal “ FPGA Based Complex Test Pattern Generation for High Speed Fault Diagnosis in FPGA Based Memory Blocks”, International Journal of Emerging Technology and Advanced Engineering (ISSN 2250-2459, Volume 2, Issue 11, November 2012).
9. Amarendar Reddy Gundla, Karunakar Reddy S, N.L.Pratap ” Communication of Dumb & Blind People with T-T-S”, International Journal of Research in Computer and Communication technology, IJRCCT, ISSN 2278-5841, Vol 1, Issue 6, November 2012.
10. Lalitha Kumari and S Karunakar Reddy and Hari Krishna and Venkata Subash “ Time –Frequency Block Thresholding Approach for Audio Denoising”, International Journal of Advances in Science and Technology, Vol. 2, No.5, 2011.

#### **Projects guided:**

1. B.Tech – 25
2. M.Tech- 10

#### **Additional Responsibilities in the Department:**

1. II ECE A-Section Class In-charge.
2. M.Tech Coordinator (ECE Dept).