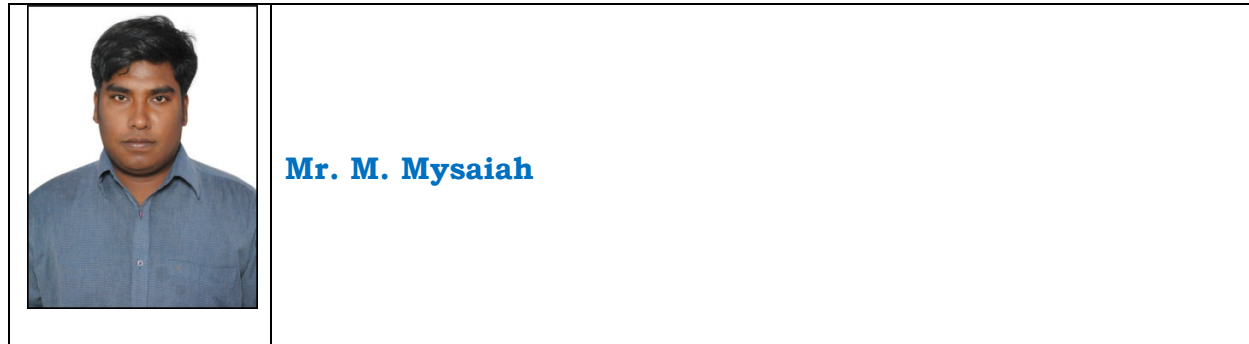


Faculty Personal Information



Designation	:	Assistant Professor
Years of Experience	:	11.5 Years
Email Id	:	mysu434moses@gmail.com
Employment Status	:	Full Time - Ratified by JNTUH
Areas of Research	:	VLSI System Design
UG Degree	:	B. Tech (Electronics and Communication Engineering)
PG Degree	:	M. Tech(VLSI SYSTEM DESIGN)
Ph.D	:	--

Subjects Taught:

1. Probability theory and Stochastic Process
2. Control systems
3. VLSI Design
4. Linear IC Applications
5. Digital Signal Processing
6. Cellular and Mobile communications
7. Satellite communications
8. Switching theory and logic design

Workshops Attended

1. Faculty development programme on “Teaching Methodologies” in 2007 June, conducted by JBIET ECE Dept., and TTTI, Chennai.

Workshops conducted:

Workshop on “Basic Electronics and PCB Design” held in February 2011 conducted by Physitech Electronics and Stanley College of Engineering and Technology for Women, Hyderabad.

Workshop on “Practical Applications of Electronics” held in February 2012 conducted by Physitech Electronics and Stanley College of Engineering and Technology for Women, Hyderabad.

Projects guided:

1. B.Tech – 25
2. M.Tech- 02

Additional Responsibilities in the Department:

1. In-charge of News Letter.
2. Coordinator of web Portal (ECE Dept).