# Faculty Personal Information



## Mr. RAVI BABU PALLAM

Designation	:	Associate Professor
Years of Experience	:	13 Years
Email Id	:	ravi.hannu@gmail.com
Employment Status	:	Full Time - Ratified by JNTUH
Areas of Research	:	Embedded systems-IOT
UG Degree	:	B. Tech (Electronics and Communication Engineering)
PG Degree	:	M. Tech(DSCE)
Ph.D	:	

## Subjects Taught:

Subjects taught at B.Tech level	Subjects taught at M.Tech level
Microprocessors &Interfacing	Microcontrollers for Embedded Systems
	design
Embedded Systems Design	Embedded Systems
Optical Communications	Digital System Design
Embedded & Real Time Systems	Embedded & Real Time Operating
	Systems
Microcontrollers & Applications	Coding Theory Techniques
Embedded Systems	
Microprocessors & Micro	
controllers	

#### • No. of publications in International journals:

- 1. (IJVDS;ISSN 2322 0929)-Implementation of Blowfish Algorithm on Micro blaze Processor.
- 2. (IJRAET; Volume 4, Issue 2 JULY 2015)-Design of Secure Differential Logic Gates for DPA Resistant Circuits.
- 3. (IJEIT;ISSN: 2277-3754)-Implementation of a Novel Algorithm in the Design of DPA Resistant Circuits for Improved Security.
- **4.** (IJATIR-2015;ISSN 2348-2370;October-2016)-Integrated Sensing Systems And Algorithms For Solid Waste Bin State Management Automation.

### • No. of publications in International Conferences:

- 1. (ICCII-2016) "IOT-Based Traffic Signal Control Technique for Helping Emergency Vehicles" Springer Science Business + Media Singapore 2017, Proceedings of the First International Conference on Computational Intelligence and Informatics, Advances in Intelligent Systems and Computing 507, DOI 10.1007/978-981-10-2471-9\_42.
- **2.** (ICVSP-KU)-Implementation of An Adaptive Filter For Pattern Processing In An 'N' Element Patch Antenna Array.
- **3.** (ICIECE-GNITS)-Design of blanking interface module (BIM) & duty cycle measurement (DCM) module for external system interface unit for receiver Application.
- **4.** (ICIECE-GNITS) -Pattern processing in a patch antenna array using space-Frequency Adaptive Processor.

1. Projects guided: 1.B. Tech – 20

2.M.Tech- 05

Memberships in Professional Bodies: Life member of ISTE.

## Additional Responsibilities in the Department: 1.In-charge of MPMC lab.

**2.**Proctoring

#### **Academic Achievements:**

- Qualified GATE twice and secured all India rank 9760(GATE sscore-224) in 2007 and 3723 in (GATE score 321) in 2008.
- Received Rs 2000/- cash reward twice from ACE Engineering College for obtaining 100% pass percentage.
- Appreciation from ACE Engineering College for successfully organizing national level technical symposium and actively participating in the placement activities.