



**Mr.Y.Pradeep**

Designation	:	Associate Professor
Years of Experience	:	12 Years 6Months
Email Id	:	pradeepyata@gmail.com
Employment Status	:	Full Time - Ratified by JNTUH
Areas of Research	:	Low Power VLSI, Mixed Signal IC Design
UG Degree	:	B.Tech (Electronics and Communication Engineering)
PG Degree	:	M.Tech (VLSI System Design)

#### **Subjects Taught:**

1. VLSI Design (B.Tech)
2. IC Applications (B.Tech)
3. Digital Design Using Verilog HDL (B.Tech)
4. Digital Communications (B.Tech)
5. Electronic Devices and Circuits (B.Tech)
6. Digital System Design (M.Tech)
7. CMOS Digital IC Design(M.Tech)
8. CMOS Analog IC Design (M.Tech)
9. CPLD & FPGA Architectures and Applications (M.Tech)
10. VLSI Technology and Design (M.Tech)
11. Low Power VLSI Design(M.Tech)
12. Algorithms for VLSI Design Automation (M.Tech)
13. Embedded System Design (M.Tech)

#### **No of Publications on International Journals:**

1."Flex Ray Communication System in Automotives : Design and Development of Power Window Control System" **International Journal** of Power System Operation and Energy Management, ISSN (*PRINT*): 2231-4407, Volume-1, Issue-2, 2011.

#### **Projects guided:**

1. B.Tech Projects: **25**
2. M.Tech Projects: **20** (VLSI) and **15** (ES)

**Academic Achievements:**

1. GATE 2007- Merit Scholar
2. GATE/UGC Scholarships Coordinator for the Four (04) Academic Years
3. M.TECH Coordinator for VLSI System Design and Embedded Systems Branches for Five (05) Academic Years
4. ECE HOD for one(01)Academic Year

**Additional Responsibilities in the Department:**

1. B.Tech Class In charge
2. In charge- ICA & HDL Simulation Lab
3. Proctor for I and II B.Tech ECE