Faculty Personal Information

Mr. B. Srinivasa Rao

Designation	:	Associate Professor
Years of Experience	:	14
Email Id	:	bsr337@gmail.com
Employment Status	:	Full Time - Ratified by JNTUH
Areas of Research	:	VLSI System Design
UG Degree	:	B. Tech (Electronics and Communication Engineering)
PG Degree	:	M. Tech(DIGITAL SYSTEMS & COMUPTER ELECTRONICS)
Ph.D	:	PURSUING- LOW POWER VLSI

Subjects Taught:

1. VLSI Design
2. Digital Logic Design
3. Switching theory and logic design
4. Embedded System Design
5. Microprocessors and Microcontrollers
6. Analog Communications
7. Electronic Measurements & Instrumentation

No. of publications :05

Papers presented in different National & International conferences/Journals

•Reduction of Leakage Power in Combinational Circuits, IOSR Journal of Electronics and Communication Engineering (IOSR-JECE) e-ISSN: 2278-2834,p- ISSN: 2278-8735.Volume 11, Issue 4, Ver. II (Jul.-Aug .2016), PP 22-26

•One of my paper accepted in– at the IEEE International Conference on Inventive Systems and Control (ICISC 2017) on 19-20 January, 2017 "New approach to reduce

Leakage Power in Combinational and Sequential circuits".

•Low Power Vehicle Security System Using ARM Processor and GSM Technology National level Conference on " Recent Developments in Electronics and Communication Engineering, on 28th & 29th March, 2014, at IETE Hyderabad.

•Low power 4 bit booth multiplier, "National Conference on Computing, Communication and Instrumentation (NCCCI-12) held on 21st & 22nd December-2012. At GITAM University".

•Low power Asynchronous Dynamic Adders International Conference on Mobile Internet Devices (ICMID-2010), organized by Gokaraju Rangaraju institute of Engineering and Technology and scheduled to be held from December 17-18, 2010 at Hyderabad.

•Sub threshold Leakage power minimization in Digital Circuits, International Conference (SPVL- 2010) Guru Nanak Engineering College

Projects guided:

1. B.Tech – 15

2. M.Tech- 04

FDPs/ Workshops attended / conducted:

Workshops attended:

1) A Two-day workshop on " VLSI Physical Design" conducted by ACE Engineering College ECE Dept. with Mentor Graphics, Hyderabad in February 2020.

2) A 3week workshop on "VLSI & Embedded system design" at JNTU in 2008 May, conducted by JNTU ECE Dept and UGC.

3) A one day workshop on "Recent trends in measurement instrumentation " Conducted by ECE Dept., JNTU, and Agilent Technologies(one of the leading Company in the world , in the instrumentational elements design) in 2009 March.

4) A three-day workshop on Analog and Mixed signal design jointly organized by ECE Dept., JNTU, and Cadence software systems Bangalore. in the month of August 2009.

5) A 3week workshop on "Emphasis on Free and Open source softwares for Research and Development" at JNTU in 2011 December , conducted by JNTU ECE Dept and UGC.

Workshops conducted:

- A Three day workshop on "Microcontroller based Autonomous Robotics" conducted by JBIET ECE Dept. and Robotic wares pvt. Ltd., Bhubaneswar, Orissa, in September 2009.

- A one day workshop on "Electronic devices and circuits " for II B.Tech students conducted by NREC, ECE Dept. by Dr.P. Satish Kumar, Hyderabad March 2011.

- A Two day workshop on "Tools for VLSI design" for M.Tech students conducted by NREC, ECE Dept. and Somarouthu Technologies., Hyderabad March 2012.

- A Two day workshop on "Microcontrollers for Embedded system design" for B.Tech students conducted by NREC, ECE Dept. and Somarouthu Technologies., Hyderabad March 2012.
- A Two day workshop on "Hands on experience on STM 32 Microcontroller"

conducted by ACE Engineering College ECE Dept. with Prudhvi Electronics, hyderabad in Febrauary 2019.

Attended Faculty Development Programmes:

1) One week online FDP on "Recent Trends in Electronics & Communication Engineering(26.04.2020 to 02.05.2020)" conducted by JBIET.

2) A 2week FDP on "TWO WEEK IIT KGP WORKSHOP ON CMOS,MIXED SIGNAL RF VLSI DESIGN" at VNR VJIET in 2017 January - February , conducted by IITKGP and VNRVJIET .

Memberships in Professional Bodies:

Additional Responsibilities in the Department:
　　　　　　1. Coordinator of Examination Branch

Extra Activities
❖ I am setting the external examination question paper for various subjects for Malla Reddy Engineering College (MREC main campus), Hyderabad.
❖ And JB Institute of Engineering and Technology

Technical Skills:

Electronics Software   Packages　　　: VHDL, Verilog HDL, ALP

Software Tools　　　　　　　　: Active-HDL, Modelsim, Xilinx, Multisim, Chip

scope-Pro, MASM, Keil, Mentor Graphics