Faculty Personal Information

 Mr. M. Mysaiah

Designation	:	Assistant Professor
Years of Experience	:	14. 2 Years
Email Id	:	mysu434moses@gmail.com
Employment Status	:	Full Time - Ratified by JNTUH
Areas of Research	:	VLSI System Design
UG Degree	:	B. Tech (Electronics and Communication Engineering)
PG Degree	:	M. Tech(VLSI SYSTEN DESIGN)

Subjects Taught:

1. Probability theory and Stochastic Process
2. Control systems
3. VLSI Design
4. Linear IC Applications
5. Digital Signal Processing
6. Cellular and Mobile communications
7. Satellite communications
8. Switching theory and logic design
9. Radar Systems
10. Optical Communications
11. CPLD & FPGA Architectures and Applications (M.Tech)

NPTEL Courses completed :

Integrated Circuits, MOSFETs, OP-Amps and their Applications (12 Weeks)

Paper Publications :

1. Published a paper on 'Implementation Of 2's Complement Of 4-Bit Binary Numbers Using Majority Logic', ISSN NO: 0377-9254, Journal of engineering services, September 2019.

2. Published a paper on "Performance Approach of AODV, DSR and DSDV protocols IEEE 8021.15.4 for vehicular networks Using NS2" Vol.6, No.1, E-ISSN: 2321-9637, January 2018.

Workshops Attended :

1. A two week workshop on "Simulation of Wireless Sensor Networks using SN2" conducted by DEPARTMENT OF ECE, SNIST, Hyderabad, Telangana State. from 20/11/2017 to 02/12/2017 .

2. One day workshop on "Python for Beginners" (19 May 2020 by BrainOvision).

Workshops conducted:

1. Workshop on "Basic Electronics and PCB Design" held in February 2011 conducted by Physitech Electronics and Stanley College of Engineering and Technology for Women, Hyderabad.

2. Workshop on "Practical Applications of Electronics" held in February 2012 conducted by Physitech Electronics and Stanley College of Engineering and Technology for Women, Hyderabad

Faculty Development Programs attended:

1. Five Day National level Faculty Development Programme on "Artificial Intelligence" (22 to 26 May 2020 by CMR Engineering College in collaboration with NYCI & Brainovision Solutions India Pvt.Ltd .

2. Five Day Faculty Development Program on "Advanced python programming using Django " (21 to 25 May 2020 by Malineni lakshmaiah women's engineering college).

3. Five Day Faculty Development Program on "Innovative trends in Data Analysis with AI " (26 to 30 May 2020 by Malineni lakshmaiah women's engineering college) .

4. One week online FDP on "Internet of Things (IoT) for emerging applications" (10 to 14 May 2020 by KITS, Warangal).

5. One week FDP on "Internet of Things (IoT) and Artificial Intelligence" (01 to 06 June 2020 by KL University, Guntur).

6. A Two day FDP on "VLSI ECAD LAB Curriculum of JNTU –Industrial Approach" held at SNIST during 24th and 25th January 2019.

7. Faculty development program on "Teaching Methodologies" in 2007 June, conducted by JBIET ECE Dept., and TTTI, Chennai.

Webinars Attended :

1. Participated in a webinar on "The New Face of Higher Education after COVID-19" conducted by ACHARYA NAGARJUNA UNIVERSITY on 10 May 2020.

2. Actively attended the webinar in titled "Research Issues and Career Opportunity in VLSI Design" organized by IEEE Student chapter (STB10084) of K.Ramakrishnan College of Technology, Samayapuram, Trichy on 25 June 2020.
3. Participated in a webinar on " Brain computer interface"on 5 June 2020 hosted by NITK-STEP in association with Pantech solutions.

4. Participated in a webinar on " Brain controlled robot design"on 6 June 2020 hosted by NITK-STEP in association with Pantech solutions.

Projects guided:

1. B.Tech – 27
2. M.Tech- 02

Additional Responsibilities in the Department:
1. Training and Placement coordinator (ECE Dept)
2. In-charge of News Letter.
3. Coordinator of web Portal (ECE Dept).
4. Dept. sports coordinator.
5. Class in-charge.