Mr.Y.Pradeep

Designation	:	Associate Professor
Years of Experience	:	15 Years
Email Id	:	pradeepyata@gmail.com
Employment Status	:	Full Time - Ratified by JNTUH
Areas of Research	:	Low Power VLSI, Mixed Signal IC Design
UG Degree	:	B.Tech (Electronics and Communication Engineering)
PG Degree	:	M.Tech (VLSI System Design)

Subjects Taught:

1. VLSI Design (B.Tech)
2. IC Applications (B.Tech)
3. Linear IC Applications (B.Tech)
4. Digital Design Using Verilog HDL (B.Tech)
5. Digital Communications (B.Tech)
6. Analog Communications (B.Tech)
7. Electronic Devices and Circuits (B.Tech)
8. Optical Communications(B.Tech)
9. Radar Systems(B.Tech)
10. Digital System Design (M.Tech)
11. CMOS Digital IC Design(M.Tech)
12. CMOS Analog IC Design (M.Tech)
13. CPLD & FPGA Architectures and Applications (M.Tech)
14. VLSI Technology and Design (M.Tech)
15. Low Power VLSI Design(M.Tech)
16. Algorithms for VLSI Design Automation (M.Tech)
17. Embedded System Design (M.Tech)

No of Publications on International Journals:

1."Flex Ray Communication System in Automotives : Design and Development of Power Window Control System" International Journal of Power System Operation and Energy Management, ISSN (*PRINT*): 2231–4407, Volume-1, Issue-2, 2011.

Projects guided:

1. B.Tech Projects: **35**
2. M.Tech Projects: **21** (VLSI) and **15** (ES)

Academic Achievements:

1. NPTEL Courses completed :
   - Integrated Circuits, MOSFETs, OP-Amps and their Applications (12 Weeks)
   - CMOS Digital VLSI Design (8 Weeks)

2. FDP's attended on Various Domains:
   - Research Topics in VLSI and Industry Trends
   - VLSI Digital Circuits & Testing Techniques
   - VLSI Design Flow
   - Innovative Trends in Data Analysis with AI
   - Opportunities and Challenges in Next –Generation Semi Conductor Devices
   - RF Design / VNA & Its Applications
   - Industrial IoT & Intelligent Connectivity using RASBERRY PI

3. Work Shop conducted on "Printed Circuit Board Design"
4. Work Shops attended on "Mentor Graphic IC Station Tool"; "Lab view Tool"
5. GATE 2007- Merit Scholar
6. GATE/UGC Scholarships Coordinator for the Four Academic Years(2011 -2015)
7. ECE HOD for the One Academic Year(2014-15)
8. M.TECH Coordinator for VLSI System Design and Embedded Systems Branches for the Five Academic Years(2010-2015)

Additional Responsibilities in the Department:

1. B.Tech Class In charge

2. In charge- IC Applications Lab, ECAD & VLSI Lab

3. Proctor for II,IV B.Tech ECE