

Faculty Personal Information

	Mrs. Sritulasi Adigopula	
Designation	:	Assistant Professor
Years of Experience	:	8
Email Id	:	sritulasiadigopula@gmail.com
Employment Status	:	Full Time - Ratified by JNTUH
Areas of Research	:	Wireless Communications & VLSI System Design
UG Degree	:	B. Tech (Electronics and Communication Engineering)
PG Degree	:	M. Tech(VLSI & Embedded Systems)
Ph.D	:	Pursuing from JNTUA
Subjects Taught:		
<ol style="list-style-type: none">1. VLSI Design2. Signals & Systems3. Probability Theory & Stochastic Process4. Electronic Devices & Circuits5. Linear & Digital IC Applications6. Verilog HDL7. Wireless Communications & Networks		
No. of publications in International journals:		
<ol style="list-style-type: none">1. 5		

Projects guided:

1. B.Tech – 12
2. M.Tech- 8

Memberships in Professional Bodies:

1. Associate Member in IETE

Additional Responsibilities in the Department:

1. Class In-charge of IIIECE-A
- 2.