


### Faculty Personal Information

	<b>Ms. SARITA CHAGANTIPATI</b>	
<b>Designation</b>	:	Assistant Professor
<b>Years of Experience</b>	:	11 months
<b>Email Id</b>	:	saritachagantipati@gmail.com
<b>Employment Status</b>	:	Full-time
<b>Areas of Research</b>	:	VLSI
<b>UG Degree</b>	:	B. Tech(Electronics and Communication Engineering
<b>PG Degree</b>	:	M. Tech(VLSI and Embedded Systems)
<b>Ph.D</b>	:	--
<b>Subjects Taught:</b>		
1.Basic Electrical and Electronics engineering		
<b>Projects guided: None</b>		
<b>Memberships in Professional Bodies: None</b>		

**Additional Responsibilities in the Department:**

1. Anti Ragging duties