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C	Code No: 117CZ	R13	
	JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYD	ERABAD	
	B. Tech IV Year I Semester Examinations, November/December A EMBEDDED SYSTEM DESIGN A	- 2017	Λ
	(Common to ECE, ETM)		$-\Delta$
~ •	ime: 3 Hours ote: This question paper contains two parts A and B.	. Marks: 75	<i>'</i>
	Part A is compulsory which carries 25 marks. Answer all questions in I	Part A. Part B	
	consists of 5 Units. Answer any one full question from each unit. It carries 10 marks and may have a, b, c as sub questions.	Each question	
			A
AG	AG AG PARTA AG A	(25 Marks)	A
1.a	List the characteristics of an embedded system. What is the difference between a system and an embedded system?	[2]	
c		[3] [2]	
d e		[3]	
	Explain the role of reset circuit in an embedded system. What is the difference between real time clock and watchdog time.	$ \begin{array}{ccc} & \begin{bmatrix} 2 \\ 3 \end{bmatrix} & \\ \end{array} $	
	g) /When do you use cooperative scheduling?////////////////////////////////////	$ \begin{array}{ccc} & [3] \\ & [2] \end{array} $	Journell
hj i)		[3] [2]	
j)	What is meant by concurrency of task execution in real time system?	[3]	
AG	AGAGAGAGAGAG	(50 Marks)	A
2.a) b)	What are the components of Embedded System Hardware?	[5+5]	
3.	OR Discuss the purpose of embedded systems. List the design metrics used to	compare them	
A 2005	A CO	[10]	
$A(\frac{1}{4})$	With a neat diagram, explain the architecture of a general purpose processor		
and the second of the second o	OR	er de se deserve	
5.a) b)	5 1 1 1 1 mm an man speci	fic processors. in. [5+5]	
6.a)	What are the design criteria of external brown-out protection circuit.		
(How to design and implement firmware for embedded systems?	[5+\$]	\wedge
7.	How to design and implement firmware for embedded systems? OR Explain with one example, how to change the bus frequency of the processor		/\
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z NSA		4 /7\\.	/

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