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	AQ	R  JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERAL B. Tech IV Year I Semester Examinations, October/November - 2020  EMBEDDED SYSTEM DESIGN (Common to ECE, EIE)  Max. Mark  Answer any Five Questions All Questions Carry Equal Marks	AG	A
	$\triangle \bigcirc^{1.a)}_{b)}$	Explain the classification of embedded systems based on different criteria in de an example for each.  What is the purpose of embedded systems?	tail and give	À
	2.a) b)	What is an embedded system? Explain the different applications of embedded sy Explain the quality attributes of embedded system.	stems. [8+7]	
	△ (3.a) (5 b)	Explain the different factors that need to be consider in the selection of Embedded Systems.  List the advantages of high level language based development.	memory for [8+7]	A
	4.a) b)	Explain the different on-board communication interfaces in brief. What are the differences between user level and kernel level threads?	[7+8]	
/	5.a) b) 6.	What is the role of reset circuit and Brown-out Protection Circuit in embedded sy Discuss the functions of device driver.  Explain the different Embedded Firmware Design Approaches.	ystem? [8+7] [15]	Į.
	7.a) b)	Explain Round Robin process scheduling with interrupts. Explain the different multitasking models in the operating system context.	[7+8]	
-	△ (3.a) b)	Explain the different methods of handling deadlocks. Explain the mutex-based process synchronization under windows OS.	[748]	A
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