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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B. Tech III Year II Semester Examinations, May - 2017

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(Common to ECE, ETM) Time: 3 hours

Note: This question paper contains two parts A and B.

Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries

10 marks and may have a, b, c as sub questions.

	(25	Marks)	
1.a) b) c) d) e) f) g) h) i)	Name the fourteen registers used for 8086 microprocessor? Draw the flag register bits in 8086 microprocessor. Write a program to find the factorial of 4 using 8086 microprocessor. What is the purpose of using macro's? What are the different memories used to interface 8086 microprocessor? Give the serial communication standards. Write about PSW used in 8051 microcontroller. Write TCON special function register used in 8051 microcontroller. What are different types of interrupts are used in 8051 microcontroller? Draw the timer/counter control logic diagram in 8051 microcontroller.	[2] [3] [2] [3] [2] [3] [2] [3] [2] [3]	<u> </u>
J		Marks)	
2.a) b)	Explain about the architecture of 8086 microprocessor with a neat sketch. Draw the timing diagram used for minimum mode of chip diagram of microprocessor.	f 8086 [5+5]	
3.a) b)	Explain signal description of 8086 microprocessor. What is an interrupt? Explain various types of interrupts in 8086 microprocessor.	[5+5]	A
4.a) b)	Explain various addressing modes of 8086 microprocessor. Describe the instruction set of 8086 microprocessor: i) SHR ii) ADD iii) DAA iv) CMP v) CBW	[5+5]	
5.a) b)	Write a program for finding multibyte addition using 8086 microprocessor. Write a program to print a string MICRO using 8086 microprocessor.	[5+5]	A
6.a) b)	Describe the interfacing of D/A Converter with a neat sketch. Explain about the mode 2 operation used in 8255 PPI in detail. OR	[5+5]	
7.a) b)	Explain about the architecture of 8251 USART with a neat sketch. Write a short notes on the scheme of serial data transfer.	[5+5]	Λ

