Code No: 126AG

JAWAHARLAL NEHRU TECHNOOGICAL UNIVERSITA HYDERABAD

B. Tech III Year 1 Semester Examinations, May - 2017 COMPUTER | THODS IN POWER SYSTEMS

(Electrical and Electronics Engineering)

Time: 3 hours Max. Marks: 75 Note: This question paper contains two parts A and B.

Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions.

Define the bus incidence matrix. 1.a) What are the merits for formation Y<sub>bus</sub> by direct inspection method? b) What data is necessary for power flow studies? WERSLIE HIDERIN [2] c) What is the need of DC load flows? [3] What are the various types of series reactors used for reducing the short circuit MVA? d) e)

[2] What is the significance fault current calculation and which fault is more severe? [3] Define the stability limit in power system. [2] g)

Define the transfer reactance and synchronizing power coefficient. [3] . h) [2] What are the various applications of equal area criterion? i)

What are the various methods to improve transient Stability? j) [3]

(50 Marks

For the figure 1 shown below, the impedance data is given in table. Determine Y Bus 2. matrix by singular transformation method [10]

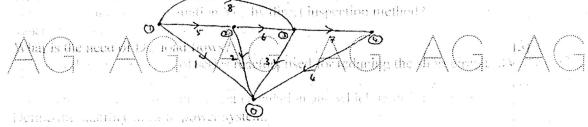
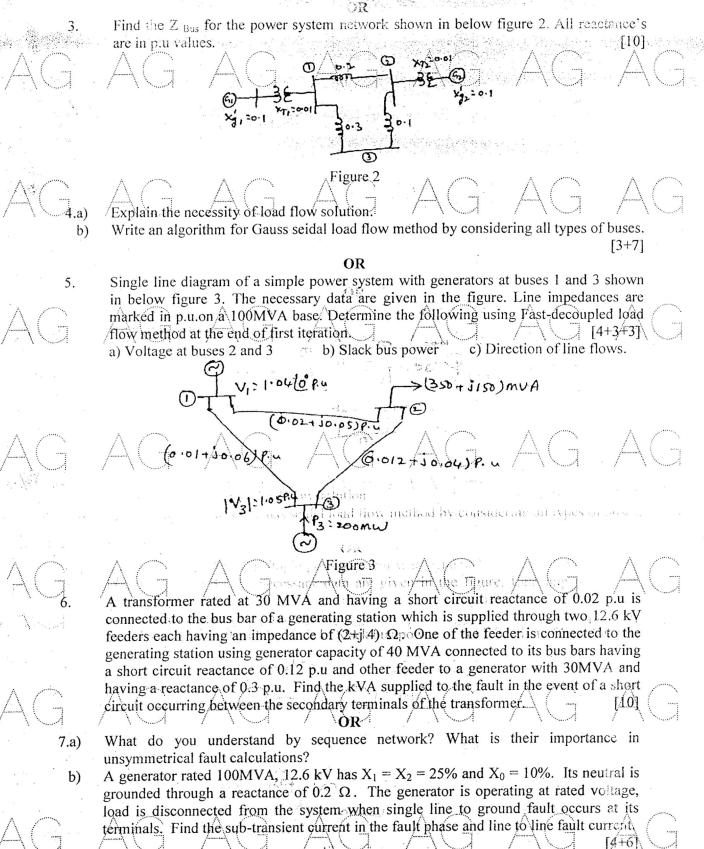


Figure 1

Table tions of equal area criterion?

Table 17 Cycle Will Live Comment			
Element	Bus code	Self impedance	/
		in p.u / \	· /-
1	0-1PART	0.1	
2	0 - 2	0.2	
3	0 - 3	0.3	
4		0.35	
5	of-2 nethod	0.4	
6 1	2-3 <sub>A</sub>	√0.1 ∧·	. /
7/\ (,	3-4	-0.2	
8 / /	1-3	0.15	/



Leaders each having an impedance of (2+) 4)  $\Omega$ . One of the feeder is connected to the

of 0.1.2 plu and other feeder to a generator with

