R18 Code No: 153AP JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech II Year I Semester Examinations, March - 2022 ELECTRICAL CIRCUIT ANALYSIS (Electrical and Electronics Engineering) Max. Marks: 75 Time: 3 Hours Answer any five questions All questions carry equal marks State and explain superposition theorem. Using mesh analysis, find the current '(' in the circuit shown in figure 1 $10\,\Omega$ 9Ω 20 V 60 Figure: 1 2.a) State and explain maximum power transfer theorem. Using Thevenin's theorem, find the current in 10Ω resistor in the circuit shown in b) figure 2. 9 Ω 3Ω 10 Ω 10 V 3.a) Derive the expression for the transient current in series RL circuit with AC excitation. Find the expression for the current i(t) in the circuit shown in figure 3. The initial voltage b) across the capacitor is 10 V and the initial current in the inductor is 5A. [8+7]Figure: 3

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