

# ACE

Engineering College Ankushapur(V), Ghatkesar(M), R.R.Dist - 501 301

(An Autonomous Institution)

# **B.TECH. THIRD YEAR DEGREE COURSE ELECTRICAL AND ELECTRONICS ENGINEERING COURSE STRUCTURE**

# (R20 Regulation)

	III Yea	r			I Semester					
S.No.	Course	Course Code	Course	Per	riods p week	er	Credits			
	type	Cour	Title	L	Т	P				
1	PCC	EE501PC	Power Electronics	3	1	0	4			
2	PCC	EE502PC	Power System-II	3	1	0	4			
3	PCC	EE503PC	Electrical Measurements And Instrumentation	3	1	0	4			
4	PEC		Professional Elective-I	3	0	0	3			
5	HSM C	SM504MS	Business Economics and Financial Analysis	3	0	0	3			
6	PCC	EE505PC	Power System Lab-II	0	0	2	1			
7	PCC	EE506PC	Power Electronics Lab	0	0	2	1			
8	PCC	EE507PC	Electrical Measurements and Instrumentation Lab	0	0	2	1			
9	HSMC	EN508HS	Advanced English Communication skills Lab	0	0	2	1			
10	MC	MC509	Intellectual Property Rights	3	0	0	0			
11	MC	MC511	Artificial Intelligence	3	0	0	0			
Total				21	3	8	22			

	III	Year			II Se	meste	r
S. No.	Course type	Course Code	Course Title	Period	ls per v	week	Credits
	•, p•			L	Т	P	
1	ESC	EE601PC	Digital Signal Processing	3	0	0	3
2	ESC	EE602PC	Microprocessors and Microcontrollers	3	0	0	3
3	PCC	EE603PC	Power System Protection	3	1	0	4
4	PCC	EE604PC	Power System Operation and Control	3	0	0	3
5	PEC		Professional Elective-II	3	0	0	3
6	OEC		Open Elective-I	3	0	0	3
7	PCC	EE605PC	Electrical Systems Simulation Lab	0	0	2	1
8	ESC	EE606PC	Microprocessors and Microcontrollers	0	0	2	1
-			Lab				
9	ESC	EE607PC	Digital Signal Processing Lab	0	0	2	1
10	MC	MC610	Cyber Security	3	0	0	0
		Γ	Total	21	1	6	22

**Note:** \***MC** = **Satisfactory/Unsatisfactory** 

# **EE501PC: POWER ELECTRONICS**

B.TECH. III Y	YEAR I S	SEMESTER											
Course Co	ode	Category	Н	ours/\	Week	Credits	S       Maximum Mar         CIA       SEE       To         30       70       1         30       70       1         cuits.       2       2         zation of power in power in power       2         S       3       3         Frs; Power semicondu       MOSFET, Power IG         source, gate drive circ       3         Principles of single-p       3         fully-controlled convorting at steady stand waveforms at steady stand w	Aarks					
			L	Т	Р	С	CIA	Maximum Marks         IA       SEE       Tot         30       70       10         30       70       10         30       70       10         30       70       10         30       70       10         30       70       10         30       70       10         30       70       10         30       70       10         30       70       10         30       70       10         30       70       10         30       70       10         30       70       10         30       70       10         30       90       90         5       90       90         30       90       90         5       90       90         30       90       90         30       90       90         30       90       90         30       90       90         30       90       90         30       90       90         30       90       90					
EE501P	C	PCC	3	1	0	4	30	70	100				
Prerequisite: A	Analog El	lectronics(EE303PC), Digit	al Electro	onics (	EE40	3PC)	I	I	<u>I</u>				
Course Object 1. To ana 2. To und 3. To des 4. To des system Course Outco 1. Unders 2. Explai 3. Examin	tives: lyze the p lerstand tl ign suitab ign suita applicati mes: Upo stand oper n working ne the opo	ower electronic circuits. he principle of operation of ble power converter for effic ble power converter for et ons. on completing this course, the ration of different power ele gs of phase-controlled rectif eration of DC-DC converter	different ient cont fficient t he studen ectronics ier circui	powe rol of ransm t will device ts.	r conv powe ission be abl	rersion circu r. and utiliza e to	uits. tion of	power i	n power				
4. Compa	are differe	ent modes of operation of in	verters										
5. Judge t	the perfor	mance of ac voltage control	ller										
UNIT: I		POV	VER SW	ITCE	IING	DEVICES							
switches and the Thyristor rating for BJT and M	heir V-I o gs and pro OSFETs	characteristics - Power Dio otection, methods of SCR c	odes, Pow commutat	ver BJ ion, U	T, SC UJT as	R, Power M a trigger sc RS	MOSFE'	Γ, Powe te drive	r IGBT; circuits				
Principles of si half-controlled operation with phase and Three	ngle-pha converte RLE loa e phase d	se fully-controlled converte er with RL and RLE load ad, Effect of load and sour lual converters.	er with R d, Princi ce induc	, RL, ples o tances	and R of thro , Gen	LE load, P ee-phase fu eral idea of	rinciples Illy-cont f gating	s of sing rolled c circuits,	le-phase onverter Single				
UNIT: III		D	C-DC C	ONVE	ERTE	RS							
Introduction, e voltage, averag duty ratio cont state, relation analysis and wa	lementary ge capacit trol of ou between aveforms	y chopper with an active sw for current. Buck converter atput voltage. Boost conver duty ratio and average ou at steady state, relation betw	vitch and - Power rter - Po utput vol ween dut	diode circui wer c tage. I y ratio	e, conc t, anal ircuit, Buck-l and a	epts of dut ysis and wa analysis a Boost conve verage outp	y ratio, aveform nd wave erter - P ut volta	average s at stea eforms a ower cir ge.	inductor dy state, .t steady .cuit,				
Unit: IV			DC-AC	CON	VER	ΓERS							
: Introduction, loads, 3-phase inverters –sing modulation.	principle bridge in le pulse	e of operation, performance werters - 120- and 180-deg width modulation, multiple	e parame rees moc pulse w	ters, s le of c idth n	single operati nodula	phase brid on, Voltage tion, sinuse	ge inver e contro oidal pu	ters with l of sing lse width	h R, RL le-phase h				
UNIT: V			AC-AC	CON	VERT	TERS							
Phase Controll controllers for cyclo-converter	er (AC V R, R-L lo rs, relevai	oltage Regulator)-Introduct ads and its applications. Cy nt waveforms, circulating cu	ion, prin clo-conv urrent mo	ciple of erter-I	of oper Princip	ration of sin ole of opera tion, Advan	gle-pha tion of s tages an	se voltag ingle pha d disadv	ge ase vantages.				

#### **TEXT BOOKS:**

- 1. M. H. Rashid, "Power electronics: circuits, devices, and applications", Pearson Education India, 4<sup>th</sup> Edition, 2014.
- 2. N. Mohan and T. M. Undeland, "Power Electronics: Converters, Applications and Design", John Wiley & Sons, 3<sup>rd</sup>Edition, 2007.

## **REFERENCE BOOKS:**

- 1. R. W. Erickson and D. Maksimovic, "Fundamentals of Power Electronics", 4<sup>th</sup> Edition Springer Science & Business Media, 2007.
- 2. L. Umanand, "Power Electronics: Essentials and Applications", Wiley India, 2009.

- 1. https://nptel.ac.in/courses/108/105/108105066/
- 2. https://nptel.ac.in/courses/108/101/108101126/
- 3. <u>https://nptel.ac.in/courses/108/101/108101038/</u>

## **EE502PC: POWER SYSTEM – II**

B.TECH. III YE	CAR I SEN	<b>IESTER</b>							
Course Cod	e	Category	Но	ours/W	eek	Credits	Max	imum Mរ	arks
			L	Т	P	С	CIA	SEE	Total
EE502PC		PCC	3	1	0	4	30	70	100
Prerequisite: Po	wer Syste	m-I(EE405PC)		1	1	1			1
Course Objectiv 1. To analyze 2. To underst 3. To underst 4. To underst	es: e the perfor tand the vo tand the pe tand the pe	rmance of transmission lines ltage control and compensatio r unit representation of power r unit representation of power	n methods systems systems						
Course Outcom	es: Upon c	ompleting this course, the stud	lent will be	e able t	0				
1. Analyze tr	ansmission	n line performance							
3. Understan	d the appli	cation of per unit quantities	cuve powe	r					
4. Design ov	er voltage	protection and insulation coord	lination						
5. Determine	the fault c	urrents for symmetrical and un	nbalanced	faults					
UNIT: I		PEF	RFORMA	NCE (	OF LI	NES			
transmission line flow through a tra	s. The equansmission	ivalent circuit representation	of a long cle diagram	Line, 1 n.	A, B, (	C, D constan	its, Ferrar	nti Effect,	Power
UNIT: II		V	OLTAG	E CON	TROI	Ĺ			
Introduction – r synchronous phas ability characteri asynchronous loa	nethods o se modifie stics of o d – Compe	f voltage control, shunt and rs. Compensation In Power Sy verhead lines – Uncompensat ensation of lines.	l series c vstems: Int red transm	apacito roduct ission	ors / I ion - C line –	nductors, taj oncepts of L Symmetrica	p changin .oad comp 1 line – H	ng transf pensation Radial lin	ormers, – Load e with
UNIT: III		PER UNIT REF	PRESENT	TATIO	N OF	POWER SY	STEMS		
The one-line diag advantages of per line, short circuit junction line term	gram, impe r unit syste red line, lin inated thro	edance and reactance diagrams em. Travelling Waves on Trans ne terminated through a resista bugh a capacitance, capacitor c	s, per unit smission I nce, line o connection	quanti Lines: H connec at a T-	ties, ch Product ted to a junctio	nanging the b tion of travel a cable, refle n, Attenuatio	base of pe ling wave ection and on of trave	er unit qua es, open c refraction elling way	antities, ircuited n at T- ves.
UNIT: IV		OVERVOLTAGE PROTE	CTION A	ND IN	ISULA	ATION COC	ORDINA	ΓΙΟΝ	
Over voltage due lightning arrester coordination, vol	e to arcing , valve typ t-time curv	ground and Peterson coil, lig pe lightning arrester, ground v res.	ghtning, h vires, grou	orn gaj ind roc	ps, sur ls, cou	ge diverters, nter poise, s	rod gaps urge abso	, expulsic rbers, ins	on type ulation
UNIT: V		SYMMETRICAL COM	IPONEN	ΓS AN	D FAU	ULT CALCU	JLATIO	NS	
Significance of p components, sequ ground fault, line impedance, reacted	positive, n lence impo to line fat ors and the	egative and zero sequence co edances and sequence network ult, double line to ground faul ir location, short circuit capaci	omponents s, fault ca t, three ph ity of a bus	s, Aver lculatic ase fau s.	rage 3- ons, sec ilt, fau	-phase powe quence netwo lts on power	r in term ork equati systems,	s of sym ons, singl faults wi	imetrical le line to th fault

#### **TEXT BOOKS:**

- 1. John J. Grainger & W.D. Stevenson, "Power System Analysis", Mc Graw Hill International, 2017.
- 2. C.L. Wadhwa, "Electrical Power Systems" New Age International Pub. Co. Third Edition, 2016.

## **REFERENCE BOOKS:**

- 1. D. P. Kothari; I. J. Nagrath, "Power System Engineering", McGraw-Hill; Third edition, 26 April 2019
- 2. A.N Kani, "Power System Analysis", CBS; Reprint edition, 2020
- 3. D.P. Kothari and I. J. Nagrath, "Modern Power System Analysis", Tata Mc Graw Hill Pub. Co., New Delhi, Fourth edition, 2011

- 1. https://nptel.ac.in/courses/108/105/108105104/
- 2. <u>https://nptel.ac.in/courses/108/105/108105067/</u>

## **EE503PC: ELECTRICAL MEASUREMENTS AND INSTRUMENTATION**

B.TECH. III YEAR	I SEMESTER							
		п	ours/	Weels	Credita	Max	imum N	Touls
Course Code	Category		ours/	week	Creuits	IVIAX	Minimin IV	arks
		L	Т	Р	С	CIA	SEE	Total
EE503PC	РСС	3	1	0	4	30	70	100
	1		1 51		(552021		<u> </u>	<u> </u>
(EE302PC) & Electro	Magnetic Fields (EE305PC).	ES), Ana	logEle	ectroni	cs (EE303F	C), Elec	stricalCi	rcuits
Course Objectives:	<u> </u>							
1. To introduce the	e basic principles of all measu	uring inst	trumer	its peratio	n of basic s	nalog ar	nd digita	1
measuring instr	uments.	principio	5 01 0 <u>1</u>		li oi basic a	illalog al	lu uigita	1
3. To deal with the	he measurement of voltage, c	urrent, P	ower	factor,	power, ene	ergy and	1 magnet	tic
measurements.	he basic concents of smart an	nd digital	meter	ina				
Course Outcomes: U	pon completing this course, the	he studer	nt will	be abl	e to			
1. Illustrate differe	ent types of measuring instrum	nents, the	eir con	structi	on, operati	on and c	haracter	istics
2. Identify the inst	ruments suitable for typical n	neasurem	nents					
3. Apply the know	ledge about transducers and i	instrume	nt tran	sform	ers to use th	nem effe	ctively.	
4. Analyze smart a	and digital metering for indus	trial appl	ication	ns.				
5. Examine the op	eration of potentiometer for c	anoratio	n of In	istrum	ents.			
UNIT: I	INTRODUCTIO	ON TO I	MEA	SURI	NG INST	RUME	INTS	
Classification – deflec	ting, control and damping to	orques –	Amm	ieters a	and Voltme	eters –	PMMC,	moving
avtension of range up	- expression for the deflecting	g torque	and co	ontrol <sup>o</sup>	torque – Er Itmatars al	rors and	l comper	isations,
attracted disc type – ex	stension of range of E.S. Volt	meters.	cirosia			cenome	lei type	anu
	DOTENTIOMETE	DC 0. T	NGTT			NGEOI		
								<b>)</b>
Principle and operatio	n of D.C. Crompton's poten	itiometer	- stai	ndardi	zation – M rdinate tvr	easurem	lent of u	nknown
applications. CT and F	PT – Ratio and phase angle er	rors.	and and		tuniate typ		indardiza	
UNIT: III	MEASURI	EMENT	OF ]	POW	ER & EN	ERGY		
Single phase dynamo	meter wattmeter, LPF and U	UPF, Do	uble a	elemer	it and three	e eleme	nt dynar	nometer
wattmeter, expression	for deflecting and control	l torques	s - E	xtensi	on of rang	ge of v	vattmete	r using
Single phase inductio	n type energy meter – drivi	ing and	brakin	wers n	iues – erro	and unba	compens	ations –
testing by phantom lo	ading using R.S.S. meter. Th	hree pha	se ene	rgy m	eter – tri-v	vector m	ieter, ma	aximum
demand meters.								
UNIT: IV		DC &	AC B	BRID	GES			
Method of measuring Foster"s bridge, Kelvi	low, medium and high res n's double bridge for measu	istance - iring low	- sens v resis	itivity tance,	of Wheat- measurem	-stone"s ent of hi	bridge igh resis	– Carey stance –
Owen"s bridge. Meas	surement of capacitance and	d loss a	ngle -	-Desai	inty"s Brid	ige, And ige - W	vien"s b	ridge –
Sellering Druge.								

UNIT: V

#### TRANSDUCERS

Definition of transducers, Classification of transducers, Advantages of Electrical transducers, Characteristics and choice of transducers; Principle operation of LVDT and capacitor transducers; LVDT Applications, Strain gauge and its principle of operation, gauge factor, Thermistors, Thermocouples, Piezo electric transducers, photovoltaic, photo conductive cells, and photo diodes.

Introduction to Smart and Digital Metering: Digital Multi-meter, True RMS meters, Clamp-on meters, Digital Storage Oscilloscope

## **TEXT BOOKS:**

- 1. G. K. Banerjee, "Electrical and Electronic Measurements", PHI Learning Pvt. Ltd., 2nd Edition, 2016
- 2. S. C. Bhargava, "Electrical Measuring Instruments and Measurements", BS Publications, 2013.

#### **REFERENCE BOOKS:**

- 1. A. K. Sawhney, "Electrical & Electronic Measurement & Instruments", Dhanpat Rai & Co. Publications, 2021.
- 2. R. K. Rajput, "Electrical & Electronic Measurement & Instrumentation", S. Chand and Company Ltd., 2016.

- 1. <u>https://nptel.ac.in/courses/108/105/108105153/</u>
- 2. https://nptel.ac.in/noc/courses/noc19/SEM2/noc19-ee44/
- 3. <u>https://www.classcentral.com/course/swayam-electrical-measurement-and-electronic-instruments-14032</u>

#### EE511PE: COMPUTER ARCHITECTURE (Professional Elective-I)

B.TECH. III Y	EAR I	SEMESTER							
Course C	ode	Category	Hou	rs/W	'eek	Credits	Ma	ximum	Marks
<b>FF5</b> 11	DF	DEC	L	Т	Р	C	CIA	SEE	Total
LESII.	F EL	<b>FEC</b>	3	0	0	3	30	70	100
<b>Prerequisite:</b> D	igital I	Electronics(EE403PC)							I
Course Objecti 1. To under 2. To under 3. To under 8086. 4. To under Performe 5. To under 6. To under	ves: rstand rstand rstand ed at m rstand rstand	basic components of com the architecture of 8086 p the instruction sets, instr the representation of data nachine level. the memory organization the parallelism both in ter	puters. processo ruction at the r and I/C rms of s	or. forma nachi ) orga ingle	ats and ine lev anizati and n	l various ac vel and how on. nultiple pro	ldressir v compu ocessors	ng mode ntations	es of are
Course Outcom	nes: Uj	pon completing this cours	se, the s	tuder	nt will	be able to			
1. Understa 2. Write eff	and the ficient	concepts of microproces programs in assembly lai	sors, th nguage	eır pr of the	ncıpl 8086	es and prac	tices. nicropi	ocessor	s.
3. Organize	e a mo	dern computer system and	d be abl	e to r	elate i	t to real ex	amples.		
4. Develop	the pr	ograms in assembly lang	guage fo	or 802	286, 8	0386 and N	AIPS pr	ocessor	s in real
and prote	ected n	nodes.		1 nra	ressor				
UNIT: I			$\mathbf{N} \mathbf{TO}$	COM		ER ORGA	NIZAT	ΓΙΟΝ	
Architecture an	d fund	ction of general compu	ter svs	tem.	CISC	Vs RISC	. Data	types.	Integer
Arithmetic - M	ultipli	cation, Division, Fixed	and Fl	oatin	g-poin	it represen	tation a	and arit	hmetic,
Control unit	operat	tion, Hardware imple	mentati	on	of C	CPU with	Micr	o inst	ruction,
microprogramm	ing, Sy	ystem buses, Multi-bus o	rganiza	tion.					
UNIT: II		MEMORY AND	INPUT	Γ <b>– Ο</b>	UTPU	UT ORGA	NIZA	ΓΙΟΝ	
System memory Memory manage	, Cach ement	e memory - types and org unit, Magnetic Hard disk	ganizati ks, Opti	on, V cal D	irtual isks.	memory ar	id its in	plemen	itation,
Input – Output	Orga	nization Accessing I/O c nd Interrupt Controllers	levices, Arbitra	Dire	ct Me Multi	mory Acce	ss and I Archited	JMA ture In	terface
circuits - Paralle	and s	erial port. Features of PC	I and P	CI Ex	press	bus.	10mmee	ture, m	
UNIT: III		16 AN	D 32 M	ICR	OPRO	OCESSOR	S		
80x86 Architect BIU, Real mode	ture, IA addre	A - 32 and $IA - 64$ , Progressing, Segmentation, add	grammi dressing	ng m g moo	odel, les of	Concurren 80x86, Ins	t operat tructior	ion of H	EU and 80x86,
I/O addressing i	n 80x8	36							
UNIT: IV			PI	PEL	ININ	G			

Introduction to pipelining, Instruction level pipelining (ILP), compiler techniques for ILP,Data hazards, Dynamic scheduling, Dependability, Branch cost, Branch Prediction, Influence on

instruction set.

## UNIT: V

#### DIFFERENT ARCHITECTURES

VLIW Architecture, DSP Architecture, SoC architecture, MIPS Processor and programming

## **TEXT BOOKS:**

- 1. V. Carl, G. Zvonko and S. G. Zaky, "Computer organization", McGraw Hill, 1978.
- 2. B. Brey and C. R. Sarma, "The Intel microprocessors", Pearson Education, 2000.

#### **REFERENCE BOOKS:**

- 1. 1. J. L. Hennessy and D. A. Patterson, "Computer Architecture A Quantitative Approach", Morgan Kauffman, 2011.
- 2. W. Stallings, "Computer organization", PHI, 1987.
- 3. P. Barry and P. Crowley, "Modern Embedded Computing", Morgan Kaufmann, 2012.
- 4. N. Mathivanan, "Microprocessors, PC Hardware and Interfacing", Prentice Hall, 2004.
- 5. Y. C. Lieu and G. A. Gibson, "Microcomputer Systems: The 8086/8088 Family", Prentice Hall India, 1986.
- 6. J. Uffenbeck, "The 8086/8088 Design, Programming, Interfacing", Prentice Hall, 1987.
- 7. B. Govindarajalu, "IBM PC and Clones", Tata McGraw Hill, 1991.
- 8. P. Able, "8086 Assembly Language Programming", Prentice Hall India.

## **EE512PE: HIGH VOLTAGE ENGINEERING** (Professional Elective-I)

B.TECH. III	YEAR	SEMESTER		,					
Course	Code	Category	Ho	urs/W	'eek	Credits	Ma	ximum	Marks
DD 51	<b>ADE</b>	DEC	L	Т	Р	С	CIA	SEE	Total
EE21	ZPE	PEC	3	0	0	3	30	70	100
Prerequisite:	Power S	Systems – I (EE405PC), E	lectro	Magn	etic Fi	elds (EE30	)5PC)		
Course Objec 7. To anal 8. To info 9. To und 10. To intro	tives: lyze bre orm abou erstand oduce h	akdown phenomenon gase at generation and measure lightning surges and swite igh voltage testing method	eous, li ement o ching o ds.	iquids of high over-v	and so n volta oltage	olid dielect ge and cur s.	rics. rent.		
Course Outco 6. Unders 7. Differe 8. Develo 9. Analyz 10. Describ	mes: Up tand bre ntiate th p tests c e the ge po prote	pon completing this course, eakdown incident in solid, he generation and measure on H. V. equipment and in neration of over-voltages.	the stud liquid ement of nsulatir in a po	dent w and g of D. ( ng mat ower s	ill be a gaseous C., A.C terials, ystem	ble to s insulating C., & Impu as per the	g materi lse volta standar	als. ages. ds.	
UNIT: I		FUNDAMENT	ALS O	F INS	SULA	TING MA	TERIA	ALS	
Ionization pro materials, Bre mechanism, Co composite diele Partial discharg	eakdown orona d ectrics, ge, appli	and de-ionization proce n in Uniform gap, no ischarge. Breakdown in p intrinsic breakdown, elec ications of insulating mate	esses, on-unifo pure an etromeo erials.	Types orm nd con chanic	s of J gaps, mmerc al brea	Discharge, Townsenc ial liquids akdown an	Gases l"s the , Solid d therm	as ins ory, St dielectr al break	sulating treamer tics and town,
UNIT: II		GENERA	ATION	N OF	HIGH	I VOLTA	GES		
Generation of currents, trippi	high D. ng and	C. and A.C. voltages, ger control of impulse genera	neration tors.	n of ir	npulse	e voltages,	generat	ion of ii	mpulse
UNIT: III		MEASUREMENTS	OF H	IGH V	VOLT	AGES AN	ID CUF	RENT	'S
Peak voltage, graphs for imp factor, partial c	impulse oulse vo lischarg	voltage and high direct tage and current measure e measurements.	current,	t meas measu	sureme	ent methoo nt of dielec	l, catho etric cor	de ray o Istant ai	scillo- nd loss
UNIT: IV		LIGHTNING A	ND SV	VITC	HING	GOVER-V	OLTA	GES	
Charge formativoltages, Prote	ion in ection ag	clouds, stepped leader, D gainst over-voltages, Surg	art lea ge divei	ider, L rters, a	.ightni and Su	ng Surges rge modifi	. Switclers.	hing ov	'er-
UNIT: V	ł	HIGH VOLTAGE TEST	FING (	OF EI	LECT	RICAL C	ОМРО	NENTS	5
Various standa and bushings, some high vol testing facility	ards for testing tage eq require	HV Testing of electrical of isolators and circuit l uipment, High voltage la nents, safety precautions	l appar breaker aborato in H. V	atus, rs, tes ory la 7. Lab	IS, IE ting o yout, s	C standard f cables, p indoor and	ls, testin oower tr l outdoo	ng of in ransforr or labor	nsulators ners and atories,

#### **TEXT BOOKS:**

- 3. "M. S. Naidu","V.Kamaraju", High Voltage Engineering, McGraw Hill Education, 2020 6<sup>th</sup> Edition
- 4. "C. L. Wadhwa", High Voltage Engineering, New Age Science, 2010

# **REFERENCE BOOKS:**

- 9. "John Kuffel", High Voltage Engineering Fundamentals, Elsevier, 3rd Ed2012
- 10. "E. Kuffel", "W. S. Zaengl", "J.Kuffel", "High Voltage Engineering Fundamentals", Newnes Publication, 2000.
- 11. "R. Arora", "W.Mosch", "High Voltage and Electrical Insulation Engineering", John Wiley & Sons, 2011.

- 1. https://nptel.ac.in/courses/108/104/108104048/
- 2. https://ietresearch.onlinelibrary.wiley.com/journal/23977264

## EE513PE: SPECIAL ELECTRICAL MACHINES (Professional Elective-I)

<b>B.TECH. III YEAR I</b>	SEMESTER		,					
Course Code	Category	H	ours/V	Week	Credits	Max	kimum N	Aarks
		L	Т	Р	С	CIA	SEE	Total
EE513PE	PEC	3	0	0	3	30	70	100
Prerequisite: Electrical	Machines-I(EE304PC), Ele	ectrical M	achin	es-II(H	EE402PC)	1	1	
Course Objectives:								
<ol> <li>To learnthe co</li> <li>To understand v</li> <li>To illustrate the</li> <li>To understand v</li> </ol>	Instruction and operation of working and application of E application of special mach working principle of stepper	of PMD BLDC mo nines motor	C mo otor	tor				
Course Outcomes: Up 1. Apply the work 2. Analyze the per 3. Derive emf equ 4. Develop contro 5. Analyze the ope	on the completion of this s ing principle of PMDC mot formance of a BLDC motor ation of various special mac lling technique to PMSM m eration of stepper motor.	subject, t or. :. chines. otor.	he str	ıdent	will be ab	le		
UNIT: I	SPECI	AL PUF	RPOS	E DC	с мотор	RS		
and applications Brushless DC motor- applications UNIT: II	BLDC Motors-construct	ion-Prin NENT N	ciple IAGI	of op	AC MOT	haracter	ristics a	nd
Permanent magnet Sy characteristics, applica	nchronous Motor- PMSM tions and control techniqu	M Motoi ues.	s-con	struct	ion-Princi	ple of	operatio	n-
UNIT: III	SWITCI	HED RE	LUC	TAN	СЕ МОТ	OR		
Introduction-construc reluctance motors- Sc principle of operation	tion-operation-application ome distinctive difference of SRM	n – imp es betwe	rover en SR	nents and	in the de conventio	esign o nal relu	f conve	entional motors-
UNIT: IV	S	ГЕРРЕІ	R MO	TOR	S			
Stepper Motors: Introc construction, principles satisfactory operation control-different config open-loop controller for	luction-synchronous induct of operation, Energization of a 2-phase hybrid step p gurations for switching the a 2-phase stepping motor. SYNCHRONOUS REI	or (or hy n with tw motor- w phase wi	ybrid vo pha ery sl ndings	steppe ase at ow sp s-contr MOT	er motor), a timeesse beed synch rol circuits OR &LIN	Hybrid ential co ronous for step <b>VER IN</b>	stepping onditions motor fo pping mo	g motor, g for the or servo otors-an
UNIT: V	Motor Construction Ward	M		DRS	Torous	anotice	Control	Dimast
Axis Current Control, F Linear induction mot	ast Torque Response Contro tors (LIM) Construction	ol, Advar – Princij	sor D tages ple of	f oper	ation $- D_{i}$	ouble s	ided LI	M from

rotating type Induction Motor – Schematic of LIM drive for traction – Development of one-sided LIM with back iron-equivalent circuit of LIM.

## **TEXT BOOKS:**

- 1. K. Venkataratnam, Special electrical machines, university press, 2009
- 2. R. K. Rajput Electrical machines, Laxmi Publications, 5th Edition 2016

## **REFERENCE BOOKS:**

- 1. V.V. Athani Stepper motor: Fundamentals, Applications and Design, New age International publishers, 1997
- 2. "E. G. Janardanan", Special electrical machines-PHI 2014.

# WEB REFERENCES:

1. <u>https://nptel.ac.in/courses/108/102/108102156/</u>

## **EE514PE: LINEAR SYSTEMS ANALYSIS**

(Professional Elective I)

B.TECH. III Y	YEAR IS	SEMESTER			-)				
Course Co	ode	Category	Н	ours/V	Week	Credits	Max	imum N	Aarks
			L	Т	Р	C	CIA	SEE	Total
EE514P	E	PEC	3	0	0	3	30	70	100
Prerequisite: ]	Mathema	tics – II (MA201BS) Electr	rical Circ	uits(F	E302	PC)			
Course Object	tives:			uns(L	1502				
1. To deve	lop abilit	ty to analyze linear systems	and signa	als		1 1.		1 .	1
2. 10 deve 3. To unde	rstand ab	al understanding of mathem out Fourier Series and Four	iatical me	sform	to and	entation	systems	and sigi	lais
4. To unde	rstand, L	aplace transform and its app	olications		1				
Course Outco	mes: Up and State	on completing this course, the Variable Analysis	he studen	t will	be abl	e to			
2. Apply n	hathemati	ical modeling tools to repres	sent linea	r syste	ems				
3. Employ	mathema	tical modeling tools to analy	yze linea	r syste	ms				
4. Underst	and the co	oncepts of Fourier Series, F	ourier Tr	ansfor	m rep	resentation	Laplace	e transfo	rm
5. Know a	bout sam	pling theorem.							
UNIT: I		STA	ATE VA	RIAB	LE A	NALYSIS			
Choice of stat	e variabl	es in Electrical Networks-	Formulat	tion of Soluti	f state	e equations	for Ele	ctrical n	etworks
networks with	state vari	able approach.	netnod -	Soluti	on oi	state equa	lions-An	alysis o	1 simple
	 I								
UNIT: II	F	OURIER SERIES AND F	OURIEI APP	R TRA PLICA	ANSF ATIO	ORM REP NS	RESEN	ΤΑΤΙΟ	N AND
Introduction, 7	rigonom	etric form of Fourier series	s, Expon	ential	form	of Fourier	series, V	Wave sy	mmetry,
Fourier integra	ls and tra	instorms, Fourier transform	of a per	iodic 1	unctionals F	on ,Properti Sourier, tran	es of Fo	urier Tra	instorm,
Laplace Transf	orem, r	ourier transform of some	commo	1 5151	uis, 1	ouner trai	5101111 1	ciutionsi	np with
Introduction, E	ffective	value, and average values of	f non sin	usoida	l perio	odic waves,	currents	s, Power	Factor,
Effects of harm	ionics, A	pplication in Circuit Analys	is, Circui	it Ana	lysis u	ising Fourie	r Series.		
UNIT: III	]	LAPLACE TRANSFORM	1 APPLI	CATI	ONS	AND NET	WORK	SYNTH	IESIS
Application of	Laplace	transform Methods of An	alysis –	Respo	nse o	f RL, RC,	RLC No	etworks	to Step,
Ramp, and in	npulse fi	anctions, Shifting Theorem	n - Con	ivolut	ion li ositiv	ntegral – A	Applicat	ions Te	sting of
Sturm"s Test.	examples	s.	porynon	nais-p	ositiv	e lear func	10118-110	opernes-	resung-
Synthesis of or	ne port L	C networks-Foster and Cau	ermetho	ds-Syr	thesis	s of RL and	RC one	e port ne	tworks-
Foster and Cau	er metho	ds							
UNIT: IV			S.	AMPI	LING				
Sampling theor	em – Gra	aphical and Analytical proo	f for Ban	ld Lim	ited S	ignal impu	se samp	ling, nat	ural and
Flat top Samp	oling, Re	construction of signal fro	m its sa	imples	s, effe	ct of unde	r sampl	ling - A	Aliasing,
correlation fun	ction, En	ergy density spectrum, Pow	ver densi	ty spe	ctrum	, Relation b	etween	auto cor	relation
function and Er	nergy / Po	ower spectral density function	on.	- 1					

UNIT: V

#### **Z-TRANSFORMS**

Fundamental difference between continuous and discrete time signals, discrete time complex, exponential and sinusoidal signals, periodicity of discrete time complex exponential, concept of Z Transform of a discrete sequence. Distinction between Laplace, Fourier, and Z-Transforms. Region of convergence in Z-Transforms, constraints on ROC for various classes of signals, Inverse Z-Transform properties of Z-Transforms.

## **TEXT BOOKS:**

- 1. "B. P. Lathi", "Signals, Systems and Communications", BS Publications 2020.
- 2. "Umesh Sinha" "Network Analysis and Synthesis", Satya Prakashan Publications, 2013.

#### **REFERENCE BOOKS:**

- 1. "A. N. Tripathi", "Linear System Analysis", New Age International, 2nd Edition 2010.
- 2. "D. Roy Chowdhary", "Network and Systems", New Age International, 2013.
- 3. "Gopal G Bhise, Prem R. Chadha", Engineering Network Analysis and Filter Design, Umesh Publications 2012.

- 1. https://nptel.ac.in/courses/108/106/108106162/
- 2. https://onlinecourses.nptel.ac.in/noc19\_ee43

# SM504MS: BUSINESS ECONOMICS AND FINANCIAL ANALYSIS

B.TECH. III	YEAR	I SEMESTER							
Course Co	ode	Category	H k	ours/	Wee	Credits	Ma	ximum	Marks
			L	Т	Р	С	CIA	SEE	Total
SM504N	/18	HSMC	3	0	0	3	30	70	100
Prerequisite:	Nil								
Course Objec	tives:								
1. To lear specific 2. To ana	n the ba cally. lyze the	sic business types, impac	t of the o	econo	my or	n Business	and Fi	rms	
Course Outer					· · · · · · · · · · · · · · · · · · ·	ha abla ta			
1. Unders Busine learnt. 2. The Str of a Co	udents company.	e various Forms of Busi Demand, Supply, Produce an study the firm's finat	iness an uction, ( ncial pos	d the Cost, sition	impa Mark by ar	act of econ act Structu nalysing th	nomic v re, Prio e Finar	variable cing asp ncial Sta	s on the bects are atements
Unit: I		INTRODUCTIO	ON TO I	BUSI	NESS	S AND EC	ONON	AICS	
Companies, Son Significance of National Incom Business Cycle	urces of Econor e, Infla . Nature	Capital for a Company, mics, Micro and Macro tion, Money Supply and e and Scope of Business	Non-Co Econom Inflatio S Econom	nvent nic Co n, Bu mics,	oncep sines Role	Sources o ts, Concep s Cycle, F of Busine	f Finan ots and eatures ess Eco	ce. Econ Import and Ph momist,	nomics: ance of nases of
Unit: II	ry nature	DEMAN	ND ANI	) SUI	PPLY	ANALYS	SIS		
Elasticity of Significance of Demand in de Steps in Dema Supply, Supply	Demano of Elast cision n nd Fore y Functi	d: Elasticity, Types of icity of Demand, Facto naking, Demand Forecas casting, Methods of Dem on and Law of Supply.	Elastic: ors affec ting: Ch and Fore	ity, I eting aracte ecasti	Law Elasti eristic ng. Si	of Deman icity of D ics of Good upply Ana	d, Me bemand Deman lysis: D	asureme , Elastie nd Fore Determin	ent and city of casting, ants of
Unit: III		PRODUCTION, COST	Г, MAR	KET	STR	UCTURE	S AND	PRIC	NG
Production, Co Production Fun Scale, Different run Cost Funct Monopoly, Olig based Pricing, E	ost, Ma ction, P t Types tions. M gopoly, Break Ev	rket Structures & Prici roduction Function with of Production Functions. larket Structures: Nature Monopolistic Competitio ven Analysis, Cost Volum	ng Proc one var Cost an of Con on. Prici ne Profit	luctic iable alysis npetit ng: T Anal	on Ai input s: Typ ion, I Types ysis.	nalysis: Fa , two varia bes of Cost Features o of Pricing	actors ible inp s, Shor f Perfec , Produ	of Proc outs, Ret t run an ct comp ict Life	luction, turns to d Long betition, Cycle
		- 11							

Accounting concepts and Conventions, Accounting Equation, Double-Entry system of Accounting, Rules for maintaining Books of Accounts, Journal, Posting to Ledger, Preparation of Trial Balance, Elements of Financial Statements, Preparation of Final Accounts.

UNIT-V

## FINANCIAL ANALYSIS THROUGH RATIOS

Concept of Ratio Analysis, Importance, Liquidity Ratios, Turnover Ratios, Profitability Ratios, Proprietary Ratios, Solvency, Leverage Ratios – Analysis and Interpretation (simple problems).

## **TEXT BOOKS:**

- 1. D. D. Chaturvedi, S. L. Gupta, Business Economics Theory and Applications, International Book House Pvt. Ltd. 2013.
- 2. Dhanesh K Khatri, Financial Accounting, Tata Mc –Graw Hill, 2011.
- 3. Geethika Ghosh, Piyali Gosh, Purba Roy Choudhury, Managerial Economics, 2e, Tata Mc Graw Hill Education Pvt. Ltd. 2012.

## **REFERENCE BOOKS:**

- 1. Paresh Shah, Financial Accounting for Management 2e, Oxford Press, 2015.
- 2. S. N. Maheshwari, Sunil K Maheshwari, Sharad K Maheshwari, Financial Accounting, 5e, Vikas Publications, 2013

- 1. https://www.slideshare.net/glory1988/managerial-economics-and-financial analysis.
- 2. https://thenthata.web4kurd.net/mypdf/managerial-economics-and-financial analysis.

## **EE505PC: POWER SYSTEM LAB-II**

<b>B.TECH. III YEAR</b>	I SEMESTER							
Course Code	Category	H k	ours	/Wee	Credits	its Maximum CIA SEE 30 70	Marks	
		L	Т	Р	С	CIA	SEE	Total
EE505PC	РСС	0	0	2	1	30	70	100
<b>Prerequisite:</b> Power S Electrical Machines-I(	ystem-I(EE405PC),Power S EE304PC), Electrical Ma	System-I chines-1	I(EE5	502PC	E),			<u>.</u>
Course Objectives:	,2200110), 210041044				-);			
1. To find sequence	the impedances of $3-\Phi$ sync	chronou	s mao	chine				
2. To find sequence	$e$ impedances of 3- $\Phi$ Tran	nsforme	r					
3. To find ABCD	parameters of a transmissi	ion line						
Course Outcomes: A	t analysis of Transmissio	the stu	Ident	will1	ne able to			
1 Analyze IDMT	over current relay	, the stu	uem	WIII (				
2 Understand diff	erential protection of sing	le-nhase	e tran	sform	ner			
3 Analyze ABCD	constants of a long trans	nission	line	510111				
4 Determine char	acteristics of under voltag	e and ov	inne. Jer vo	nltage				
5. Simulate shunt	canacitor for under voltage	e contro	ol usii	ng M.	ATLAB			
List of Experime	nts:	,••••	1 4011	19 111				
L.								
1. Characteristics	of IDMT Over-Current R	Relay						
2. Differential pro	stection of $1-\Phi$ transforme	er	. 1.	( <b>T</b> . T	1			
3. Characteristics	of Micro Processor based	l Over V	oltag	ge/Un	der Voltage	e relay		
4. A, B, C, D con 5. Finding the sec	stants of a Long Transmis	sion nne		s mac	hine			
6. Finding the sec	The second seco	Transfe	orme	r.	iiiic.			
7. Simulation of I	LG, LL, LLG and LLL fa	ults on a	ı sim	ple po	ower system	n using		
PSCAD/MATI	LAB				2	C		
8. Simulation of l	oad compensation							
9. Determination	of Sequence components	(Positiv	e, Ne	gativ	e and Zero)	) of an A	Alternat	or
10. Determine AB	CD parameters of short, n	nedium	and l	ong T	ransmissio	n lines	using	
MAILAB. 11 Determine Cha	racteristics of under valta	ae and a	Wer 1	voltao	e using DQ			R
12. Simulation of a	shunt capacitor for under	ge and e r voltage	e cont	trolus	sing MATI	AB.	IAILA	
WEB REFERENCES	S:							
1. https://vp-dei.vla	bs.ac.in/Dreamweaver/lis	t.html						

# **EE506PC: POWER ELECTRONICS LAB**

Course Code	Category	H k	ours/	Wee	Credits	Max	kimum	Marks
	<b>D</b> G G		Т	Р	С	CIA	SEE	Tota
EE506PC	РСС	0	0	2	1	30	70	100
Prerequisite : Power H	Electronics(EE501PC)							
Course Objectives:	of nower electronic co	nverters f	or eff	icient	conversio	n/contr	alofno	wor
from source to load	l.			leient	conversio		51 01 p0	WCI
. Design the power of	converter with suitable	switches n	neetin	ig a sp	ecific load	l requir	ement.	
. To make the studen	nts to design triggering	circuits of	f SCR					
. To perform the exp	periments on various co	nverters.	1 4	11	1 11 4			
<b>J</b> Understand the	pon completing this col	urse, the st	udeni	I W111	be able to			
1. Understand the $($	operating principles of	various po	wer e		onic conver	riers.		
2. Analyze the cha	racteristics of MOSFE	I, IGB1, S	CK a	na SC	CR firing C	KIS,		
3. Develop the sin	nulation model power c	converters.	Naar					
4. Apply different	commutation technique	e to turn of	t SCF	۲.				
5. Examine output	of inverter for differe	nt types of	tload	s				
List of Experimen	its:		TUT					
1. Study of Chara	cteristics of SCR, MOS	SFELAR	JBI,					
2. Gate firing circ	C Voltage Controller y	vith <b>R</b> and	<b>BI I</b>	oade				
4. Single Phase h	alf controlled & fully co	ontrolled b	ridge	conve	erter with I	R and R	Lloads	
5. Forced Commu	tation circuits (Class A	A. Class B.	Class	s C. C	lass D & C	Class E)		
6. Single Phase C	yclo-converter with R	and RL los	ads	,		,		
7. Single Phase se	eries& parallel inverter	with R and	d RL	loads				
8. Single Phase B	ridge inverter with R a	nd RL loa	ds.					
9. DC Jones chop	per with R and RL Loa	ads						
10. Single Phase d	ual converter with RL l	oads						
Following experir	nent are to be done by	y using sui	itable	softv	vare.			
I. (a) Simulation	of single-phase Half w	ave conve	rter u	sing F	R and RL I	oads		
(b) Simulation	1 of single-phase full co	onverter us	ing R	., KL 8	and KLE I	oads		
(c) Simulation	n of Single phase Semi C	voltage cor	ising l strolle	K, KL	and KLE	Ioads		
(b)Simulation of	f Single phase Cyclo-co	onverter w	ith R	and R	ig K and K 21 -loads	L IUdus	•	
13. Simulation of B	auck chopper		iui iv	unu r				
14. Simulation of si	ingle-phase Inverter wi	th PWM c	ontro	1				
15. Simulation of the	hree phase fully control	lled conve	rter w	vith R	and RL lo	ads, wi	th and v	vithou
Freewheeling d			C C .	ntinu	ous and D	iscontir	nuous m	odes
i ice wheeling u	iode. Observation of wa	aveforms t	for Co	Jininu	ous und D			loues (
Operation.	iode. Observation of w	aveforms t	for Co	Jining				loues

http://vlabs.iitb.ac.in/vlabs-dev/labs/mit\_bootcamp/power\_electronics/labs/index.php

# **EE507PC: ELECTRICAL MEASUREMENTS AND INSTRUMENTATION LAB**

Course Code	Category	H	ours/\	Week	Credits	Max	kimum N	<b>Iarks</b>
		L	Т	P	С	CIA	SEE	Tota
<b>EE507PC</b>	РСС	0	0	2	1	30	70	100
rerequisite: Electrical	Measurements and Instr	umentati	on (E	E503P	PC)			
Course Objectives:							•	
1. To calibrate LPF	Watt Meter, energy meter,	, P. F Mete	er usin	ig elect	ro dynamo	meter t	ype instr	ument
2 To determine un	strument known inductance resistar	ice canac	itance	hy ner	forming ex	nerimer	ts on D	C
Bridges & A. C.I	Bridges	ice, capae	nance	by per	ioiiiiig ex	.permer		C
3. To determine three	e phase active & reactive	powers us	ing sir	ngle wa	attmeter me	ethod pra	actically	
4. To determine the	ratio and phase angle error	s of curre	nt trar	nsform	er and pote	ntial trai	nsformer	:
Course Outcomes: Upo	on completing this course,	the studen	t will	be able	e to			
1. To select instrum	ents							
2. Analyze anyelect	rical instrument							
3. Find the accuracy	of any instrument by perf	orming ex	perim	ent.				
4. Calibrate PMMC	instrument using D.C pote	entiometer						
5. Estimate the Stren	ngth of Dielectric oil							
<ol> <li>Calibration of dyr</li> <li>Crompton D.C. P</li> <li>Kelvin"s double H</li> <li>Dielectric oil testi</li> <li>Schering Bridge &amp;</li> <li>Measurement of 3</li> <li>Measurement of 3</li> <li>Calibration LPF w</li> <li>Measurement of 3</li> <li>C.T. testing using Null method.</li> <li>PT testing by com the given PT</li> <li>Resistance strain g</li> <li>Transformer turns</li> </ol>	namometer power factor m otentiometer – Calibration Bridge – Measurement of r ing using H.T. testing Kit. & Anderson Bridge. B - Phase reactive power w displacement with the help vattmeter – by Phantom tes b-phase power with single mutual Inductor – Measur parison – V. G. as Null de auge – strain measurement ratio measurement using A	ith single- of LVDT sting. watt meter rement of tector – M ts and Cal	C amm – Dete – Dete – Date – Dete – Dete r and t % rati leasure ibratic s.	wattm wattm wo CT o error ement on.	nd PMMC tion of Tole eter. s. and phase of % ratio	voltmete erance. angle of error and	r. f given C 1 phase a	T by ngle c
15. Measurement of %	a ratio error and phase angl	le of given	ıСГb	y comp	barison.			
VEB REFERENCES	5:							
<ol> <li><u>http://vlabs.iitkgr</u></li> <li><u>http://vlabs.iitkgr</u></li> <li><u>http://vlabs.iitkgr</u></li> <li><u>http://vp-dei.vlal</u></li> <li><u>http://vlabs.iitkgr</u></li> </ol>	o.ernet.in/asnm/exp10/inde o.ernet.in/asnm/exp23/inde o.ernet.in/asnm/exp21/inde bs.ac.in/Dreamweaver/exp o.ernet.in/asnm/exp7/index	e <u>x.html</u> e <u>x.html</u> e <u>x.html</u> 94. <u>html</u>						

# EN508HS: ADVANCED ENGLISH COMMUNICATION SKILLS LAB

<b>Course Code</b>	Category	H k	ours/	Wee	Credits	May	kimum	Marks
	HEMO	L	Т	Р	С	CIA	SEE	Total
EN508HS	нямс	0	0	2	1	30	70	100
Prerequisite: Knowle	dge of functional English,	, basics	in gra	.mma	r, understa	nding o	fLSRW	' skills
<ol> <li>To improve the enable them to I speakers and res</li> <li>Further, they we writing.</li> <li>To prepare all th</li> <li>Course Outcomes: U</li> <li>Better understa group activitie</li> <li>Neutralization</li> <li>Speaking skills</li> </ol>	students' fluency in Engl isten to English spoken at spond appropriately in dif ould be required to commu- ne students for their place pon completing this cours anding of nuances of Engl s of accent for intelligibility s with clarity and confider	ish, thro normal ferent se unicate ments. se, the st ish lang y nce whice	bugh a l convocio-c their i tuden tuden tuden	a well ersati cultur deas t will throu urn er	l-developed ional speed al and profi relevantly a be able to gh audio- y	l vocabi by edu essional and coh visual est eir empl	ulary an cated En l contex erently xperiend	d nglish ts. in ce and ty skill
<ul> <li>Role Play in difference</li> <li>word roots, one-word analogy, idioms and p</li> <li>Activities on Reading guessing meanings fr</li> </ul>	nt situations & Discourse substitutes, prefixes and phrases, collocations & us ing Comprehension –Gen om context, scanning, ski	Skills- suffixes sage of eral Vs mming,	using , stud vocab Local infer	y of v ulary comp ring r	als - Synon vord origin prehension neaning, cr	yms an , busine , readin ritical re	d antony ess voca g for fac eading&	yms, bulary ets,
effective googling. 3. Activities on Writi writing/Resume writi improving one's writ	ng Skills – Structure and ng/ e-correspondence/Tec ing.	presenta chnical 1	ation of report	of dif writi	ferent type ng/ – plann	s of wri ning for	ting – le writing	etter ;-
4. Activities on Prese sessions/seminars/PP emails/assignments e	ntation Skills – Oral prese Ts and written presentation tc.	entations ons thro	s (ind ugh p	ividua osters	al and grou s/projects/r	p) throu eports/	ıgh JAN	1
5. Activities on Grou intervention, summar organization of ideas opening strategies, an and Mock Interviews	p Discussion and Intervie izing, modulation of voic and rubrics for evaluation swering strategies, intervi	w Skills e, body n- Conce iew thro	s – Dy langu ept an ugh te	vnami lage, 1 ld pro ele-co	cs of group relevance, ocess, pre-in onference &	o discus fluency nterviev z video	sion, and v planni -confere	ng, ence
MINIMUM REQUI	REMENT:							
The Advanced Englis infrastructural faciliti	h Communication Skills ( es to accommodate at lea	(AECS) st 35 sti	Labo idents	ratory in th	y shall have lab:	e the fol	llowing	
• Spacious room with	appropriate acoustics.							

- Round Tables with movable chairs
- Audio-visual aids
- LCD Projector
- Public Address system
- P IV Processor, Hard Disk 80 GB, RAM-512 MB Minimum, Speed 2.8 GHZ
- T. V, a digital stereo & Camcorder
- Headphones of High quality

**SUGGESTED SOFTWARE:** The software consisting of the prescribed topics elaborated above should be procured and used.

- Oxford Advanced Learner's Compass, 7th Edition
- DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice.
- Lingua TOEFL CBT Insider, by Dream tech
- TOEFL & GRE (KAPLAN, AARCO & BARRONS, USA, Cracking GRE by CLIFFS)

# **TEXT BOOKS:**

1. Effective Technical Communication by M Asharaf Rizvi. McGraw Hill Education (India) Pvt. Ltd. 2 nd Edition

2. Academic Writing: A Handbook for International Students by Stephen Bailey, Routledge, 5th Edition.

# **References:**

1. Learn Correct English – A Book of Grammar, Usage and Composition by Shiv K. Kumar and HemalathaNagarajan. Pearson 2007

2. Professional Communication by ArunaKoneru, McGraw Hill Education (India) Pvt. Ltd, 2016.

3. Technical Communication by Meenakshi Raman & Sangeeta Sharma, Oxford University Press 2009.

4. Technical Communication by Paul V. Anderson. 2007. Cengage Learning pvt. Ltd. New Delhi.

5. English Vocabulary in Use series, Cambridge University Press 2008.

6. Handbook for Technical Communication by David A. McMurrey& Joanne Buckley. 2012. Cengage Learning.

7. Communication Skills by Leena Sen, PHI Learning Pvt Ltd., New Delhi, 2009.

8. Job Hunting by ColmDownes, Cambridge University Press 2008.

9. English for Technical Communication for Engineering Students, AyshaVishwamohan, Tata Mc Graw-Hill 2009.

# MC509: INTELLECTUAL PROPERTY RIGHTS

<b>B.TECH. III YEAR I SI</b>	EMESTER							
Course Code	Category	Ног	ırs/Weel	k	Credits	Maxir	num Ma	arks
MC509	мс	L	T	P	С	CIA	SEE	Total
110307		3	0	0	0	100	0	100
		UNI	Г: І					
Introduction to Intellectua agencies and treaties, imp	al property: Introduction portance of intellectual	on, types of l property rig	intellect ghts.	ual pro	perty, inter	national o	rganizat	ions,
		UNIT:	II					
Trade Marks: Purpose ar selecting, and evaluating	nd function of traden trade mark, trade mark	narks, acqu x registration	isition o n process	of trade ses.	e mark righ	nts, prote	ctable n	natter,
		UNIT: I	II					
Law of copy rights: Fun perform the work publi international copy right 1 rights and transfer	damental of copy right icly, copy right own aw. Law of patents: F	ht law, orig ership issu Foundation	inality o es, copy of patent	f mater v right t law, p	rial, rights registration patent searc	of reprodu n, notice hing proc	uction, r of cop cess, own	ights to y right, nership
		UNIT: I	V					
Trade Secrets: Trade secr secrets, protection for su Publicity, false advertisin	et law, determination bmission, trade secret g.	of trade sec e litigation.	rete statu Unfair c	is, liabi competi	lity for mist tion: Misap	appropria propriatio	tions of on right	trade of
		UNIT:	V					
New development of inte intellectual property audit right law, international pa	llectual property: new s. International overvio ttent law, and internation	developme ew on intell onal develo	nts in tra ectual pr pment in	de mar operty, trade s	k law; copy internation secrets law.	v right law al – trade	y, patent mark la	law, w, copy
<ol> <li>Intellectual propert</li> <li>Intellectual propert</li> <li>Publishing company</li> </ol>	y right, Deborah. E. Bo y right – Unleashing th y ltd.	ouchoux, Co ne knowledg	engage le ge econoi	earning my, pra	ibuddha gar	nguli, Tata	a McGra	w Hill

# MC511: ARTIFICIAL INTELLIGENCE

Course Code	Category	Hou	urs/Weel	k	Credits	Maxii	num Ma	arks
		L	Т	P	С	CIA	SEE	Tota
MC511	МС	3	0	0	0	100	0	100
			Ů	Ű	Ŭ	100		100
Prerequisite: Nil								
1. To train the studer fundamentals of knowledge repres applied AI.	nts to understand different knowledge representat sentation, reasoning. Stu	nt types of A ion, buildir idy of Mark	AI agents ng of sin ov Mode	, variou nple kn ls enabl	us AI searc owledge-ba le the studer	h algorit sed syste at ready to	hms, ms and o step int	to apr o
UNIT: I								
ntroduction: AI proble	ms, Agents and Enviro	onments, St	tructure	of Age	nts, Probler	n Solving	g Agent	s Bas
Search Strategies: Prob	lem Spaces, Uninform	ed Search	(Breadth	-First,	Depth-First	Search,	Depth-fi	rst wi
terative Deepening), H	euristic Search (Hill Cl	imbing, Ge	neric Be	st-First,	A*), Const	traint Sat	isfaction	
Backtracking, Local Se	arch)							
UNIT: II								
Alpha-Beta Pruning Bas Forward Chaining and E	sic Knowledge Represe Backward Chaining, Intr	entation and oduction to	Reasoni Probabil	ng: Pro istic Re	positional I asoning, Ba	Logic, Fir yes Theo	st-Order rem	Logi
Advanced Knowledge	Representation and R	Reasoning.	Knowled	dae Re	presentation	n Issues	Nonmo	notor
Reasoning, Other Know Acting Under Uncertair Networks	wledge Representation nty, Bayes' Rule, Repre	Schemes esenting Kn	Reasonir owledge	ng Und in an U	ler Uncerta Uncertain D	inty: Ba omain, E	asic pro	babili
UNIT: IV								
Learning: What Is Learn Learning from Example	ning? Rote Learning, Le s, Winston's Learning F	earning by T Program, De	Taking A	dvice, I rees.	Learning in	Problem	Solving,	
UNIT: V								
Expert Systems: Repres	enting and Using Doma	in Knowled	ge, Shell	, Expla	nation, Kno	wledge A	cquisitio	on.
<b>FEXT BOOKS:</b> 1. Russell, S. and Nor	rvig, P, Artificial Intelli	gence: A M	odern Ap	proach	, Third Edit	ion, Pren	ticeHall,	2010.

# **EE601PC: DIGITAL SIGNAL PROCESSING**

<b>B.TECH. III YEA</b>	R II SEMESTER							
Course Code	Catagomy	H	ours/	Wee	Credits	Max	ximum	Marks
Course Code	Category	k						1
		L	Т	Р	C	CIA	SEE	Total
EE601PC	ESC	3	0	0	3	30	70	100
Prerequisite: Nil								
Course Objectives	he having of Signals and Su	atoma no	~~~~~	d for	all Electric	al En a	maanina	malatad
courses	the basics of Signals and Sy	stems re	quire	u lor	an Electric	arengi	ineering	, related
2. To understa	nd the behavior of signal in	time and	freat	iencv	domain			
3. To understa	nd the characteristics of LTI	systems	5	5				
4. This gives of	concepts of Signals and Syst	ems and	its an	alysis	s using diff	erent tr	ansform	1
techniques.						_		
5. To study th	e designs and structures of	f digital	(IIR a	and F	FIR) filters	and an	nalyze a	and
synthesize f	or a given specifications.	.1						
Course Outcomes	: Upon completing this cours	se, the st	udent	W1II	be able to			
2 Represent a	e various signal functions.	nd freque	ency d	omai	n			
3. Understand	the characteristics of linear t	time inva	ariant	svste	ms.			
4. Analyze the	signals with different transf	form tech	nnique	- <i>j</i>				
5. Design a dig	gital filter for a given specifi	cation.						
UNIT: I	SIGNAL ANALYSIS, F	OURIE	R SE	RIES	5, FOURIE	ER TRA	ANSFO	RMS
Classification of Sig	nals and systems, Exponenti	al and Si	nusoi	dal si	gnals, Con	cepts of	f Impuls	se
function, Unit Step	function, Signum function.							
Fourier series: Rep	resentation of Fourier series	, Contin	uous t	time p	periodic sig	nals, Pi	ropertie	s of
Fourier Series, Diric	hlet's conditions, Trigonome	etric Fou	rier S	eries	and Expon	ential F	ourier S	eries,
Complex Fourier sp	ectrum.	_					_	
Fourier Transform	s: Deriving Fourier Transfo	orm from	Four	ier se	ries, Fouri	er Tran	sform o	f
arbitrary signal, Fou	rier Transform of standard si	ignals, F	ourier	Tran	storm of P	eriodic	Signals	,
Properties of Fourie	r Transform, Fourier Transf	orms inv	olvin	g Imp	oulse funct	ion and	Signun	1
tunction.		·	1	10				
Frequency Domain I	Representation of Discrete Tr	ime Sign	als ar	nd Sys	stems	AVAT		
UNIT: II	SIGNAL I KANSMIS	SIUN I		DUGE	1 LINEAF Dem	( 5 1 5 1	EMS,	
Signal Transmissio	n through Lincon Systems		ING	INE	JKENI			
Linear System Imp	lse response Response of a	Linear	Sveter	n Lir	hear Time	Invaria	at(I TI)	
System Linear Time	- Variant (I TV) System Tr	ansfer fu	nction	n, En 1 of a	I TI Syste	m Filte	m(L11)	
characteristic of Line	ear System Concept of conv	volution i	n Tin	ne doi	nain and F	requenc	ev doma	nin
Sampling theorem:	Graphical and analytical pr	oof for F	Band I	Limite	ed Signals	Impuls	e aona	•111
Sampling. Natural a	d Flat top Sampling, Recon	struction	ofsi	enal f	rom its sar	nples. F	Effect of	2
under sampling – Al	iasing,		2	9		<b>F</b> , -		
UNIT: III	LAPLACE	<b>FRANS</b>	FORM	MS, Z	-TRANS	FORM	S	
Laplace Transfor	ms: Laplace Transforms (L	.T). Inve	rse L	aplac	e Transfor	m. Con	cept of	Region
of Convergence (R	OC) for Laplace Transform	is. Prope	erties	of L	L. Relation	betwee	en L.T :	and F.T
of a signal Laplace	Transform of certain signal	s using y	vavef	or L.	unthesis			
<b>7</b> Transformar C	an agent of Z. Transform of	o Digoro	ta Sa		ynthesis.	tion has	trucon I	anlass
Equipar and 7 T	undept of Z- Hallstoffill Of			quent Tress	former C			Lapiace,
Fourier and Z Ira	instorms, kegton of Conve	D	In Z-	rans	norm, Cor	istraints	s on R	JC IOT
various classes of s	Ignais, Inverse Z-transform,	Propert	es of	Z-trai	nstorms.			
UNIT: IV	L DIGITAL FILTERS ANI	D REAL	IZA	FION	OF DIG	ITAL F	FILTEF	RS

**IIR Digital Filters:** Analog filter approximations – Butterworth and Chebyshev, Design of IIR Digital Filters from Analog Filters, Step and Impulse Invariant Techniques, Bilinear Transformation Method, Spectral Transformations.

Realization of Digital Filters: Realization of Digital Filters – Direct, Canonic, Cascade and Parallel Forms.

UNIT: V

## FIR DIGITAL FILTERS

FIR Digital Filters: Characteristics of FIR Digital Filters, Frequency Response. Design of FIR

Filters: Fourier Method, Digital Filters using Window Techniques, Comparison of IIR & FIR filters. **TEXT BOOKS:** 

- 1. Signals, Systems & Communications B.P. Lathi, 2013, BSP.
- 2. Signals and Systems A.V. Oppenheim, A.S. Willsky and S.H. Nawabi, 2 Ed.
- 3. Discrete Time Signal Processing A. V. Oppenheim and R.W. Schaffer, PHI, 2009
- 4. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education / PHI, 2007.

## **REFERENCE BOOKS:**

- 1. Signals and Systems Simon Haykin and Van Veen, Wiley 2 Ed.,
- 2. Signals and Systems A. Rama Krishna Rao, 2008, TMH
- 3. Fundamentals of Signals and Systems Michel J. Robert, 2008, MGH International Edition.
- 4. Signals, Systems and Transforms C. L. Philips, J.M.Parr and Eve A.Riskin, 3 Ed., 2004, PE.
- 5. Signals and Systems K. Deergha Rao, Birkhauser, 2018.
- 6. Digital Signal Processing Fundamentals and Applications Li Tan, Elsevier, 2008
- 7. Fundamentals of Digital Signal Processing using MATLAB Robert J. Schilling, Sandra L. Harris, Thomson, 2007
- 8. Digital Signal Processing S. Salivahanan, A. Vallavaraj and C. Gnanapriya, TMH, 2009
- 9. Digital Signal Processing A Practical approach, Emmanuel C. Ifeachor and Barrie W. Jervis, 2<sup>nd</sup> Edition, Pearson Education, 2009

- 1. https://nptel.ac.in/courses/108/106/108106151/
- https://nptel.ac.in/courses/108/101/108101174/ 2.
- https://nptel.ac.in/courses/117/101/117101055/ 3.

# **EE602PC: MICROPROCESSORS AND MICROCONTROLLERS**

<b>B.TECH. III YEAR</b>	II SEMESTER										
Course Code	Category	H k	ours/	Wee	Credits	Ma	ximum	Marks			
EE602DC	ESC	L	Т	Р	С	CIA	SEE	Total			
EE0021 C	ESC	3 0 0 3 30 70									
<b>Prerequisite:</b> Digital Knowledge	System Design, Computer	r Organi	zatior	n, Bas	ics of VLS	I, Basio	e Progra	umming			
Course Objectives:											
1. To develop an u	inderstanding of the funct	ionality	of mi	cropro	ocessors; A	ssembl	y langu	age			
2. To provide kno	nd interfacing techniques wledge on functionality of	f microc	ontro	llers;	Assembly	anguag	ge				
programming a	nd interfacing techniques	•									
3. To develop an u	inderstanding of the opera	tions an	d Pro	gram	ming of AF	RM Pro	cessor				
4. To study the ba applications.	sic concepts of Advanced	ARM p	rocess	sors (	A, R, M pr	ofile) a	nd their				
Course Outcomes: U	pon completing this cours	se, the st	udent	will	be able to						
1. Understand the set& assembler	8086µp architecture, its o directives to write Progra	peration ams usin	and a g MA	apply SM.	the knowle	edge of	instruct	tion			
2. Understand the setto design and	8051µc architecture, its o	operation	and	apply	the knowle	edge of	instruct	tion			
3 Apply the know	vledge of 8051 uc and Con	municat	tion n	rotoc	ols to inter	face I/C	) device	28			
4. Understand the	ARM processor internal a	architect	ure: a	nnlvt	he knowle	dge of i	nstructi	ion set			
todesign applic	ations.		u10, u	ppij (		450 011	11561 4001	on see			
5. Understand the theirapplication	ARM CORTEX and OM	AP Proc	essor	archi	tecture and	archite	ecture ai	nd			
UNIT: I 808	36 MICROPROCESSO	R ARCH	IITE	CTUI	RE & INS'	TRUC	ΓΙΟΝ S	SET			
8086 Architecture: 80	086 Architecture-Function	al diagra	am, R	egiste	er Organiza	tion, N	lemory				
Segmentation, Program	nming Model, Memory ad	ldresses,	Phys	ical N	/lemory Or	ganizat	ion,				
Architecture of 8086, S	signal descriptions of 8080	6, interrı	ipts o	f 8080	5.						
Instruction Set and A	ssembly Language Prog	rammin	g of 8	8086:	Instruction	format	s, Addr	essing			
Branch and Call Instruc	Assembler Directives, Metions, Sorting, String Mar	acros, ar nipulatio	nd Sin ons.	nple I	rograms in	ivolvin	g Logic	al,			
UNIT: II 8051 M	/IICROCONTROLLER	ARCH	ITEC	TUR	E & REA	L TIM	E CON	TROL			
Introduction to Micro	controllers: Overview of	f 8051 N	licroc	ontro	ller, Archi	tecture,	I/O Por	rts,			
Memory Organization,	Addressing Modes and Ir	1structio	n set o	of 803	)]. mina Exta	mal Ua	duvora				
Interrupts Programmir	of: Programming Timer I	ion Inter	s, Pro runte	Prog	ming Exter	паг па 051 Ті	ners an	d			
Counters	ig the Serial Communication		rupis,	Tiog		051 11	ners an	u			
UNIT: III	I/O, MEMOR	Y & SE	RIAL	BUS	S INTERF.	ACE					
I/O and Memory Inte	rface: LCD, Keyboard, E	xternal N	Memo	ory RA	AM, ROM	Interfac	e, ADC	C, DAC			
Serial Communication	n and Bus Interface: Ser	ial Com	munic	ation	Standards	Serial	Data Tr	ansfer			
Scheme, On board Cor Interfaces-RS232 USB	nmunication Interfaces-I2	C Bus, S	SPI B	us, U	ART; Exte	rnal Co	mmuni	cation			
UNIT: IV	AR	M ARC	HITI	ЕСТІ	JRE						

ARM Processor fundamentals, ARM Architecture – Register, CPSR, Pipeline, exceptions and interrupts interrupt vector table, ARM instruction set – Data processing, Branch instructions, load store instructions, Software interrupt instructions, Program status register instructions, loading constants, Conditional execution, Introduction to Thumb instructions.

#### UNIT: V

#### ADVANCED ARM PROCESSORS

Introduction to CORTEX Processor and its architecture, OMAP Processor and its Architecture.

## **TEXT BOOKS:**

- 1. Advanced Microprocessors and Peripherals A. K. Ray and K.M. Bhurchandani, MHE, 2ndEdition
  - 2006.
- 2. The 8051 Microcontroller, Kenneth. J. Ayala, Cengage Learning, 3rd Ed.
- 3. ARM System Developers guide, Andrew N SLOSS, Dominic SYMES, ChrisWRIGHT, Elsevier, 2012

## **REFERENCE BOOKS:**

- 1. Microprocessors and Interfacing, D. V. Hall, MGH, 2nd Edition 2006.
- 2. Introduction to Embedded Systems, Shibu K.V, MHE, 2009
- 3. The 8051Microcontrollers, Architecture and Programming and Applications -K.UmaRao, Andhe Pallavi,

Pearson, 2009.

- 1. <u>https://www.arm.com/</u>
- 2. https://www.intel.com/content/www/us/en/homepage.html
- 3. https://onlinecourses.nptel.ac.in/noc20\_ee42/preview
- 4. <u>https://ict.iitk.ac.in/courses/microprocessors-and-microcontrollers/</u>
- 5. https://en.wikipedia.org/wiki/Microcontroller
- 6. <u>https://nptel.ac.in/courses/108/103/108103157/</u>
- 7. https://nptel.ac.in/courses/106/105/106105193/

# **EE603PC: POWER SYSTEM PROTECTION**

<b>B.TECH. III Y</b>	EAR II S	EMESTER							
Course Coo	le	Category	Hou	ırs/Weel	K	Credits	Maxir	num Ma	arks
			L	Т	Р	С	CIA	SEE	Total
EE603PC		PCC	3	1	0	4	30	70	100
Prerequisite: P	ower Sys	tem-I(EE405PC), Powe	r System-	II(EE502	2PC)				
Course Objecti 2. To introdu 3. To explain 4. To unders 5. To study I	ves: ace all kin n the volta tand the p Microproc	ds of circuit breakers ar age control and compens whenomenon of Over Vo cessor Based Relays	nd relays sation met ltages and	thods 1 its class	sificatio	on			
<b>Course Outcom</b> 1. Compare	es: Upon electroma	completing this course gnetic, static and micro	, the stude	nt will b -based re	e able 1 lavs	to			
2. Apply tech	hnology to	o protect power system	componer	nts	1				
3. Interpret r	elay settir	ngs of over current and o	distance re	elays	inonit 1	<b>-n</b> -altana			
5. Analyze t	he testing	of circuit breakers	i, on and	vacuum	incun	JIEakeis			
UNIT: I			PRO	ГЕСТІУ	E RE	LAYS			
Relay Construct relays. UNIT: II Time-current ch parallel feeders,	aracterist	tromagnetic relays, the OVER-CURRENT ics, current setting, over on of ring mains, Phase	ermal rela T PROTE er current e fault and	ECTION protective d earth f	c relay DIST ve sche fault pr	ANCE PRO	OTECTIC ional rela ombined	DN y, protection fa	tective ction of ult and
phase fault prot relay, MHO rela swings, effect or relays, MHO rel	ective sch y, input q f line leng ay with bl	uantities for various typ th and source impedance inders, Reduction of me	fault related to the period of	ance rela berforma nits, swit	nce Pro nys, Eff nce of cched d	fect of arc red distance relations	esistance, ays, select emes, auto	relay, re Effect o tion of d re-closi	f power istance ing.
UNIT: III	PILO	T RELAYING SCHE	MES AC	MACHI	NES A	AND BUS Z	ONE PR	ОТЕСІ	TION
Wire Pilot prote Generators, Prot	ction, Ca ection of t	rrier current protection ransformers, Buszone p	n. AC M protection,	achines frame le	and B akage	us Zone Pr protection.	rotection:	Protecti	ion of
UNIT: IV		STATIC RELA	YS MICF	ROPRO	CESSO	OR BASED	RELAYS	5	
Amplitude and I instantaneous co relays, static dir of Quadrilateral relays, directiona	Phase comparators ectional read and Ellip al relays, o	nparators, Duality betw s, static phase comparat elay, static differential ptical relay characteristic distance relays.	een AC an tors, coinc relay, stat cs. Micro	nd PC, S eidence t <u>y</u> ic distan processo	tatic an ype of j ce relaj r Base	nplitude co phase comp ys, Multi in d Relays: A	mparator, arator, sta put comp dvantage	integrat atic over arators, s, over	ing and current concept current

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UNIT: V
```

## **CIRCUIT BREAKERS FUSES**

Introduction, arcing in circuit breakers, arc interruption theories, re-striking and recovery voltage, resistance switching, current chopping, interruption of capacitive current, oil circuit breaker, air blast circuit breakers, SF6 circuit breaker, operating mechanism, selection of circuit breakers, high voltage d.c. breakers, ratings of circuit breakers, testing of circuit breakers. FUSES: Introduction, fuse characteristics, types of fuses, application of HRC fuses, discrimination.

## TEXT BOOKS:

- 1. "Badriram"," D.N. Vishwakarma", Power System Protection and Switchgear, McGraw Hill Education; 2nd edition, 2017
- 2. "Sunil S.Rao", Switchgear Protection And Power Systems", Khanna Publishers, 14th edition, 2019

## **REFERENCE BOOKS:**

- 1. "U.A.Bakshi", "M.V.Bakshi", Switchgear and Protection, Technical Publications, 1st edition 2021
- 2. "J.B. Gupta", Fundamentals of Switchgear and Protection, S.K. Kataria& Sons; 2013 edition, 2013
- "D.P. Kothari", "I. J. Nagrath", Modern Power System Analysis Tata Mc Graw Hill Pub. Co., New Delhi, Fourth edition, 2011

- 1. https://nptel.ac.in/courses/108/101/108101039/
- 2. https://nptel.ac.in/courses/108/105/108105167/

# **EE604PC: POWER SYSTEM OPERATION AND CONTROL**

B.TECH. III YE	CAR II SE	EMESTER							
Course Cod	e	Category	He	ours/W	eek	Credits	Maxir	num Ma	rks
			L	T	P	С	CIA	SEE	Tota
EE604PC		PCC	3	0	0	3	30	70	100
Prerequisite: Po	wer Syste	m-I(EE405PC), Power System	-II(EE502	2PC)					<u> </u>
<ol> <li>To unders</li> <li>To know t</li> <li>To analyze</li> <li>To analyze</li> </ol>	tand real p tand real p he importa e different e different	bower control and operation ance of frequency control methods to control reactive po methods to control reactive po	wer wer						
Course Outcom 1. Analyze th 2. Analyze th 3. Describe r 4. Design sur 5. Analyze S	es: Upon on the optimal the steady s eactive po itable conto CADA an	completing this course, the stud scheduling of power plants state behavior of the power syst ower control of a power system troller to dampen the frequency ad EMS functions	lent will b tem for vo r and volta	oe able oltage a age stea	to and free ady sta	quency fluc te oscillatio	tuations ns		
UNIT: I		LOA	D –FREQ	QUEN	CY CO	ONTROL			
modeling - static model.	analysis c	of uncontrolled case - tie line wi	ith freque	ncy bia	as cont	rol of two-a	rea syster	n - state v	ariable
			OWER -	- VOL			)L		<u>.</u>
- generation and method of voltag tap setting of OL and to minimize t	e power co absorptio e control TC transf ransmissi	ontrol. Excitation systems – mo on of reactive power. Relation - tap-changing transformer. Sys- former and MVAR injection of on loss.	stem level f switched	tatic an 1 volta 1 contro 1 capac	id dyna ge, po ol using vitors to	wer and re g generator o maintain a	active po voltage m acceptable	ty comper wer at a nagnitude e voltage	node - setting
UNIT: III		ECON	OMIC L	OAD	DISPA	ТСН			
Statement of eco without loss and	onomic di with loss,	spatch problem – cost of gen solution by direct method and ?	eration – ∧-iteratior	incren metho	nental od.	cost curve	- co-ordii	nation eq	uations
UNIT: IV		U	NIT COM	MMIT	MENT	Г			
Statement of Unifuel constraints approach. Numer method.	it Commit and other rical prob	tment problem – constraints; s · constraints. Solution method lems on priority-list method u	pinning r s - Prior sing full-	eserve ity-list load a	, therm metho verage	al unit con ods - forwa production	straints, h rd dynan 1 cost and	nydro con nic progra 1 Forward	straints amming d DP
UNIT: V		COMPUTER (	CONTRO	DL OF	POW	ER SYSTE	MS		
Need of compute functions - system	r control on monitor	of power systems. Concept of e ring - data acquisition and contr	energy cor ol. Syster	ntrol ce n hardy	entre (c ware co	or) load disp	atch centration - SCAD	re and the A and EN	is 1S

functions. Network topology - Importance of Load Forecasting and simple techniques of forecasting.

## **TEXT BOOKS:**

- 1. "D. P. Kothari"," I. J. Nagrath", Modern Power System Analysis, 4th Edition, Tata McGraw Hill Publishing Company Limited, New Delhi, 2011
- 2. "Olle. I. Elgerd", Electric Energy Systems Theory An Introduction, Tata McGraw Hill Publishing Company Ltd, New Delhi, 2nd edition, 2017

# **REFERENCE BOOKS:**

- 1. "Chakrabarti &Haldar", Power System Analysis: Operation and Control, Prentice Hall of India, Third edition. January 2010
- 2. "C.L. Wadhwa", Electrical Power Systems New Age International Pub. Co. Third Edition, 2016
- 3. "Sivanagaraju", Power System Operation and Control, Pearson Education India; 1st edition 2010

- 1. https://nptel.ac.in/courses/108/101/108101040/
- 2. https://nptel.ac.in/courses/108/104/108104052/

# **EE611PE: OPTIMIZATION TECHNIQUES**

## (Professional Elective II)

<b>B.TECH. III YEAR</b>	II SEMESTER		, ,					
Course Code	Category	Но	ırs/W	eek	Credits	Ma	ximum	Marks
EE(11DE	DEC	L	Т	Р	С	CIA	SEE	Total
LEOIIPE	PEC	3	0	0	3	30	70	100
Prerequisite: Mathem	atics –I(MA101BS) & I	Mathema	tics –I	I(MA	.201BS)			
Course Objectives: <ol> <li>To introduce transportation p</li> <li>Constrained an electrical and e</li> <li>To learn charace</li> <li>To explain the implementation</li> <li>Course Outcomes: A</li> <li>Explain the new</li> <li>Understand op</li> <li>Apply classical transportation p</li> <li>Compare and c</li> <li>Develop algori</li> <li>UNIT: I</li> <li>Statement of an Optim objective function -o</li> <li>Single variable Optim sufficient conditions constraints. Solution b</li> </ol>	various optimization problem, simplex algori d unconstrained optimi electronic engineering ci- cteristics of constrained e concept of Dynamic fiter completion of this c ed of optimization of en- timization of electrical a l optimization technique problem ontrast different unconst thm using constrained n <b>CLASSICA</b> nization problem – desi objective function surfa- nization– multivariable for minimum/maxi- py method of Lagrange – Kuhn – Tucker condit	techniq ithm, dyn ization tec ircuits des problems program ourse, the gineering and electro es, linear p strained op on-linear <b>L OPTI</b> ign vector aces – o Optimiz mum–mu multiplier ion.	ues amic j hniqu sign p stude syste orogra progr <b>MIZ</b> classif ation ltivar rs – N	like progra ies for robler and i ent wi ms engine ummir zation ammi ammi sign c ficatic withe iable /ultiv	classical, amming r solving a ns in real v its applicat ill be able t eering prob ng, simplex methods ing anddyn <b>N TECHN</b> constraints on of Op out constra ariable Op	linear nd opti world si tions to to olems algori amic pa amic pa <b>NQUE</b> – cons otimizat aints – ation – otimizat	progra mizing tuations projec thm, thm, rogramm <b>S</b> traint su ion pro necessa with c ion with	umming, an ; t ning urface – oblems. ary and equality 1
UNIT: II	L	INEAR ]	PROG	GRAN	MMING			
Standard form of programming problem simultaneous equation the simplex method – <b>Transportation Prob</b> least cost method and transportation problem	a linear program ns – definitions and as – pivotal reduction simplex algorithm. <b>Jem:</b> Finding initial b Vogel"s approximation as.	nming theorem of a gen pasic feas n method	prob s – eral s ible s – test	lem solut systen solutic	– geo ion of a n of equation on by nortion optimali	ometry a syste ions – h – we ity of ba	of em of motiva est corn alanced	linear linear tion to er rule,
UNIT: III	UNCONSTRA	INED OI	PTIM	IZAT	ΓΙΟΝ ΤΕΟ	CHNIQ	UES	
One dimensional m interpolation method:	inimization methods, Univariant method, Pov	Classifica well"s me	tion, thod a	Fibo and st	nacci me reepest des	thod a cent mo	and Qu ethod.	ıadratic

Characteristics of constrained problem -classification - Basic approach of Penalty Function method –Basic approach of Penalty Function method Basic approaches of Interior and Exterior penalty function methods -Introduction to convex programming problem.

#### UNIT: V

#### DYNAMIC PROGRAMMING

Dynamic programming multistage decision processes – types –concept of sub optimization and the principle of optimality – computational procedure indynamic programming – examples illustrating the calculus method of solution –examples illustrating the tabular method of solution

## **TEXT BOOKS:**

- 1. Singiresu S. Rao, Engineering Optimization: Theory and Practice by John Wiley and Sons, 4th edition, 2009.
- 2. H. S. Kasene& K. D. Kumar, Introductory Operations Research, Springer (India), Pvt. Ltd., 4th edition ,2004

## **REFERENCE BOOKS:**

- 1. "George Bernard Dantzig", "Mukund Narain Thapa", Linear programming, Springer series in operations research 3rd edition, 2003.
- 2. "H.A. Taha", Operations Research: An Introduction, 8th Edition, Pearson/Prentice Hall, 2007.
- 3. "KalyanmoyDeb",Optimization for Engineering Design Algorithms and Examples, PHI Learning Pvt. Ltd, New Delhi, 2005.

## **WEB REFERENCES:**

1. https://nptel.ac.in/courses/111/105/111105039/

# **EE612PE: WIND AND SOLAR ENERGY SYSTEMS**

#### (Professional Elective II)

B.TECH. III Y	EAR II	SEMESTER			,				
Course Co	ode	Category	H	ours/V	Week	Credits	Max	imum N	larks
			L	Т	Р	С	CIA	SEE	Total
EE612P	E	PEC	3	0	0	3	30	70	100
Prerequisite: 1	Nil	<u> </u>						<u> </u>	
Course Object 1. To study 2. To unde 3. To know 4. To analy 5. To discu	tives: the physes rstand the the solar the sol	sics of wind power and ener e principle of operation of w r power resources lar photo-voltaic cells lar thermal power generatio	gy vind gene n	erators					
Course Outcourse 1. Underst sources. 2. Identify 3. Apply th 4. Underst 5. Identify	mes: Upo and the en- the basic ne power and the is the netwo	on completing this course, the nergy scenario and the conse- physics of wind and solar p electronic interfaces for win ssues related to the grid-integ- ork integration issues.	ne studen equent gr oower gen nd and so gration o	t will owths neration lar gen f solar	be able s of the on. neration and w	e to e power ger on. vind energy	nerate rei	newable	energy
UNIT: I		PH	IYSICS	OF W	<b>IND</b>	POWER			
History of wind Wind speed sta	d power, tistics-pro	Indian and Global statistics obability distributions, and	, Wind p Wind po <sup>,</sup>	hysics wer-cu	s, Betz ımulat	limit ratio	, stall an ition fun	d pitch c ctions.	control,
UNIT: II		WIND	GENEF	RATO	R TO	POLOGIH	ËS		
Review of me Generators, Do Generators, Po	odern w oubly-Fec wer elect	ind turbine technologies, I Induction Generators and ronics converters. Generato	Fixed a l their cl r configu	nd V naracte	ariable eristics ns, Con	e speed w s, Permane nverter Cor	vind tur nt Magr ntrol.	bine, Ir net Sync	duction hronous
UNIT: III		SOLAR	ENERG	Y TE	CHNC	DLOGIES			
Introduction, so length, Estimat Technologies-A array, Power H algorithms. Con	blar radia ion of so Amorpho Electronic nverter C	tion spectra, solar geometry lar energy availability. us, mono-crystalline, polyc c Converters for Solar Syst ontrol.	r, Earth S rystalline tems, Ma	un an e; V-I aximu	gles, o charao m Pov	bserver Su cteristics of ver point 7	n angles f a PV co fracking	, solar da ell, PV r (MPPT	ny nodule, )
UNIT: IV		NETWO	RK INT	EGRA	<b>ATIO</b>	N ISSUES			
Overview of gr regulation, vol disturbances. F isolated operati	rid code t ltage and ower qu ons of so	rechnical requirements. Fau d frequency operating lim ality issues. Power system lar PV and wind systems.	lt ride-th nits, sola intercon	rough 1r PV nectio	for w and on exp	ind farms - wind farm eriences in	real and behave the wor	l reactiv ior duri ld. Hyb	e power ng grid rid and
UNIT: V		SOLAR THE	ERMAL	POW	'ER G	ENERAT	ION		
Technologies, System Therma Factors,Control Arrays with Sec	Parabolic al Calcula ls,Collect ctions Ha	trough, central receivers, p ations: Component Models, or Arrays: Series Connectio ving Different Orientations.	arabolic Collecto ons, Perfo	dish, I or Hea ormano	Fresne t Exch ce of P	l, solar pon hanger Fact Partially Sha	d, eleme or, Duct ided Col	ntary an and Pip lectors, S	alysis. e Loss Series

#### **TEXT BOOKS:**

- 1. "T. Ackermann", Wind Power in Power Systems, John Wiley and Sons Ltd., 2012.
- 2. "G. M. Masters", Renewable and Efficient Electric Power Systems, John Wiley and Sons, 2013.

## **REFERENCE BOOKS:**

- 1. "S. P. Sukhatme", Solar Energy: Principles of Thermal Collection and Storage, McGraw Hill, 2008.
- 2. "H. Siegfried"," R. Waddington", "Grid integration of wind energy conversion systems" John Wiley and Sons Ltd., 2006.
- 3. "J. A. Duffie","W. A. Beckman", Solar Engineering of Thermal Processes, John Wiley & Sons, 2013.

- 1. https://nptel.ac.in/noc/courses/noc21/SEM1/noc21-ch11/
- 2. <u>https://nptel.ac.in/courses/103/103/103103206/</u>

# **EE613PE: DIGITAL CONTROL SYSTEMS**

# (Professional Elective-II)

<b>B.TECH. III</b>	YEAR ]	II SEMESTER							
Course C	ode	Category	H	ours/	Wee	Credits	Max	ximum	Marks
EE613P	۶E	PEC	L	Т	Р	С	CIA	SEE	Total
	L		3	0	0	3	30	70	100
Prerequisite:	Control	Systems(EE404PC)	11					I	1
Course Object 1. To under discrete 2. To under 3. To under 4. To get e 5. To under Course Outco 1. Obtain e 2. Find the 3. Test and 4. Analyze 5. Design	ctives: erstand t erstand t erstand t erstand t erstand t comes: A discrete e state sp d analyze state fee	he fundamentals of digita x domain. he concepts of state variab he concepts of controllabi the design aspects of cont he concepts of the stability t the end of this course, st representation of LTI syst bace analysis of discrete time e the controllability and of y of discrete time systems dback controllers and obs	al contro oles anal lity and rollers a y for dis tudents tudents tems. me syste bservab using v	bl syst obser and fo crete will d ems. ility f variou	tems i vabili r disc LTIV emon	representat crete LTIV ity of discru- rete time systems strate the a crete time shods	ions, z- / syster ete time ystems bility to	transfor ns. e system	rms and
UNIT: I		REPRESENTAT	TION O	FDIS	SCRF	TE TIME	SYST	EMS	
Basics of Digit circuit. Mathen Choice of samp Z-Transforms, Transforms. Pu Discrete time sy	al Contr natical M bling free Mappin Ilse Tran ystems.	rol Systems. Discrete repr Modeling of sample and I quency. ZOH equivalent. ng from s-plane to z p nsfer function: Pulse tran Time response of discrete	resentat hold cir blane, P nsfer fu time sy	ion of cuit. Proper inction stem,	f cont Effect ties c n of c Stead	inuous sys s of Samp of Z-Trans closed loop ly State err	tems. S lling an sforms p system ors.	ample a d Quan and In ms. Sol	and hold tization. werse Z ution of
UNIT: II		DISCRETE TI	ME ST	ATE	SPA	CE ANAL	YSIS		
State space rep space models a Pulse Transfer Controllability, Effect of pole z	Function stabiliz	ion of discrete time syst -versa, Solving discrete t n Matrix. Discretization of ability, observability, rea cellation on the controllab	ems, Co time sta of contir achabili bility & o	onvers te spa nuous ty – ( observ	sion c ace ec time Contro vabilit	of pulse tra uations, S state space ollability a ty.	nnsfer f tate Tra e equati and obs	unction ansition ons. Co ervabili	to state Matrix, oncept of ity tests.
UNIT: III		STABILITY ANA	LYSIS	OF I	DISC	RETE TIN	AE SYS	STEM	
Concept of stat transformation.	bility in z Stability	z-domain, Stability analys y Analysis of discrete time	sis discı e systen	ete tin ns usin	me sy ng Ly	stem: by J apunov me	ury test ethods.	, using	bilinear

## UNIT: IV

#### DESIGN OF DIGITAL CONTROL SYSTEM BY CONVENTIONAL METHODS

Design and realization of digital PID Controller, Design of discrete time controllers with bilinear transformation, Design of digital control system with dead beat response, Practical issues with dead beat response design.

# UNIT: V DEISGN STATE FEEDBACK CONTROLLERS AND OBSERVERS

Design of discrete state feedback controllers through pole placement, Design of Discrete Observer for LTI System: Design of full order and reduced observers, Design of observer-based controllers.

## **TEXT BOOKS:**

- 1. "K. Ogata", Digital Control Engineering, Prentice Hall, Englewood Cliffs, 1995.
- 2. "M. Gopal", Digital Control Engineering, Wiley Eastern, 1988.
- 3. "V, I, George". "C. P. Kurian", Digital Control Systems, CENGAGE Learning, 2012

## **REFERENCE BOOKS:**

- 1. "G. F. Franklin", "J. D. Powell", "M. L. Workman", Digital Control of Dynamic Systems, Addison-Wesley, 1998.
- 2. "B.C. Kuo", Digital Control System, Holt, Rinehart and Winston, 1980.

## WEB REFERENCES:

1. https://nptel.ac.in/courses/108/103/108103008/

# EE614PE: VLSI DESIGN

# (Professional Elective II)

EE614PE		k		Cicuit	Max Mark		<b>VIAI NS</b>	
EE614PE		k T	T	р	<u> </u>		SE	T-4-1
EE014PE	DEC		I	r	C		E E	1 otal
	PEC	3	0	0	3	30	70	100
Prerequisite : Digital E	Electronics(EE403PC)					I		
Course Obiectives :								
1. Give exposure t	o different steps involved	in the t	fabrio	cation	of ICs using	MOS tran	sistor,	
CMOS/BICMO	S transistors.				-			
2. Explain electric	al properties of MOS and	BiCM	DS de	evice	s to analyze th	e behavio	r of inve	rters
designed with v	arious loads.							
3. Give exposure t	to the design rules to be fo	llowed	to dr	aw th	he layout of an	y logic cii	cuit.	.1 .
4. Provide concept	t to design different types	of logi	c gat	es us	ng CMOS inv	verter and	analyze	their
5 Provide design	eristics.	a bloc	zs of	data	nath of any sy	stem using	rates	
6. Understand bas	ic programmable logic dev	vices ar	nd tes	sting	of CMOS circ	uits.	gates.	
Course Outcomes :	non successful completion	of the	0011	se st	udents will be	able to:		
1. Acquire qualitat	tive knowledge about the	fabrica	tion 1	broce	ss of integrate	ed circuits	using M	IOS
transistors.	i ve kilowiedge ubout tile	1001100	lion	proce	ss of integrate	a eneans	using in	100
2. Draw the layou	t of any logic circuit whic	h helps	s to u	nders	stand and estin	nate paras	itic of ar	y logic
circuit		•				-		
3. Design different	types of logic gates using	CMOS	S inv	erter	and analyze th	neir transfe	er charac	teristics
4. Design building	blocks of data path subsys	stems a	nd m	iemo	ries using basi	c digital lo	ogic devi	ces.
5. Design simple lo	ogic circuits using PLA,PA	AL, FP	GA a	nd C	PLD		t aftart	
	erent types of faults that ca		5 111 11 TN	i sysi		the concep	ot of testi	ng.
UNIT-1 ntroduction to IC Ter	hnology MOS PMOS	NMO		$\frac{1}{100}$	& BICMOS			
	$childs_{y} = WOS, TWOS,$		5, CN	105	a biewos.			
Basic Electrical Pro	perties of MOS and	BiCM	OS (	Circ	uits: Ids-Vds	relationsh	ips,MOS	transisto
threshold Voltage, gn	n, gds, Figure of merit;1	Pass tr	ansis	tor,N	MOS Inverte	r, Variou	s pull u	ps, CMO
Inverter analysis and d	esign, Bi-CMOS Inverter	s.						
						GEGGEG		
UNIT-II		El com	IRC		DESIGN PRO	DCESSES		D1
VLSI Circuit Design	Processes: VLSI Design	FIOW,	MO	D La	iyers, Stick D	hagrams,	Design	Kules and
Layout, I ransistors La	yout Diagrams for N	MOS a	and (	CMO	S Inverters a	nd Gates,	Scaling	g of MOS
circuits.								
UNIT- III			GAT	EL	EVEL DESIG	N		
Gate Level Design :L	ogic Gates and Other com	nplex ga	ates.	Swite	ch logic, Alter	nate gate o	vircuits.	Гіте
delays. Driving large	capacitive loads. Wiring c	apacita	ance.	Fan	– in. Fan – out	t. Choice o	flavers	
<b>3</b> , 88	1, , 8	I	,		,	,	5	
		TH SU	JBSY	ZSTE	MS & ARRA	VSURG	VCTEM	
UNIT- IV	DATAPA						ISIEN	S
UNIT- IV Data Path Subsystem	DATAPA s: Subsystem Design, Sh	nifters,	Add	ers, A	ALUs, Multip	liers, Pari	ty genera	S ators,
UNIT- IV Data Path Subsystem Comparators, Zero/On	<b>DATAPA</b> <b>as:</b> Subsystem Design, She Detectors, Counters.	nifters,	Add	ers, A	ALUs, Multip	liers, Pari	ty genera	S ators,
UNIT- IV Data Path Subsystem Comparators, Zero/On Array Subsystems: SH	<b>DATAPA</b> <b>Is:</b> Subsystem Design, Sh e Detectors, Counters. RAM, DRAM, ROM, Seri	nifters,	Add ess M	ers, A	ALUs, Multip	liers, Pari	ty genera	S ators,
UNIT- IV Data Path Subsystem Comparators, Zero/On Array Subsystems: SI	<b>DATAPA</b> <b>as:</b> Subsystem Design, Sh e Detectors, Counters. RAM, DRAM, ROM, Seri	nifters, al Acco	Add ess M	ers, A	ALUs, Multip	liers, Pari	ty genera	<u>S</u> ators,

Programmable Logic Devices: Design Approach – PLA, PAL, Standard Cells, FPGAs, CPLDs.

CMOS Testing: CMOS Testing, Test Principles, Design Strategies for test, Chip level Test Techniques.

## **Text Books :**

- 1. Essentials of VLSI circuits and systems Kamran Eshraghian, EshraghianDougles and A.Pucknell, PHI, 2005.
- 2. CMOS VLSI Design A Circuits and Systems Perspective, Neil H. E Weste, David Harris, Ayan Banerjee, 3rd Ed., Pearson, 2009.

## **Reference Books** :

- 1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective Ming-BO Lin, CRC Press, 2011.
- 2. CMOS Logic circuit Design John .P. Uyemura, Springer, 2007.
- 3. Modern VLSI Design Wayne Wolf, Pearson Education, 3rd Ed., 1997.

## Web References :

- 1. <u>https://nptel.ac.in/courses/117/101/117101058/</u>
- 2. https://nptel.ac.in/courses/108/107/108107129/
- 3. http://www.vlsi-expert.com/p/vlsi-basic.html

## E-text Books :

- 1. https://www.phindia.com/Books/ShoweBooks/MTMzMA/MTE2NA/VLSI-Design
- 2. http://www.cmosvlsi.com/
- 3. https://www.springer.com/gp/book/9781402084461
- 4. https://books.google.co.in/books?id=CO8zq6\_vcr8C&printsec=frontcover

# **EE605PC: ELECTRICAL SYSTEMS SIMULATION LAB**

<b>B.TECH. III YEAR</b>	R II SEMESTER								
Course Code	Category	H	Hours/Week		Credits	Maximum Marks			
		L	Т	Р	С	CIA	SEE	Total	
EE605PC	РСС	0	0	2	1	30	70	100	
			Ű	_	-	50	, ,	100	
Prerequisite: Power	System-I(EE405PC), Power Syste	m-II(EE50	2PC),						
Course Objectives:	: MATLAB/PSCAD/PSPICE/PSIN	VI							
1. To perform vo	oltage distributions across insulator	strings							
2. To understand	I the high frequency transients	8-							
3. To perform pa	rameter estimation and fault analys	sis on Trans	smissio	on line	s				
4. To perform pa	rameter estimation and fault analys	sis on Trans	smissio	on line	S				
Course Outcomes:	Upon completing this course, the st	udent will	be able	e to					
1. Analyze vario	us transmission line calculations								
2. Determine tim	e constants for RL, RC and RLC c	ircuits							
3. analyze the V	/oltage distribution across insula	ator string							
4. Determine fau	lt currents of transmission line								
5. Analyze the e	xperimental data and draw the conc	clusions							
List of Experim	ents:								
1. Generation of	high frequency transients through	RLC circui	t						
2. Voltage distri	oution across insulator string								
3. Comparison o	f lumped and distributed transmissi	ion lines							
4. Calculation of	4. Calculation of fault currents of transmission line								
5. Time constant	5. Time constant calculation of RL circuit								
6. Time constant	calculation of RC circuit								
7. Time constant	calculation of RLC circuit								
8. Simulation of	Resonance circuit								
9. Calculation of	R, L, C, Zs of 3-phase Transmission	on Line							
10. Estimation of	TARIFF based on load curve								

# EE606PC: MICROPROCESSORS AND MICROCONTROLLERS LAB

<b>Course Code</b>	Category	Hours/	Week	2	Credits	Ma	ximum	Marks
FEACDO	FSC	L	Т	Р	С	CIA	SEE	Total
ELOUOPC	ESC	0	0	2	1	30	70	100
rerequisite: Digital	Electronics(EE403PC)	)						
ourse Objectives:								
1. To develop an u	inderstanding of the As	ssembly lar	iguag	e prog	gramming	on		
8086Microproc	essor. 	taufa ain a ta	. <b>h</b> . n. i			1:		
2. To develop an u	inderstanding of the A	sembly lan	cnniq	ues w	ramming	using K	ocessor.	on
8051µc.	inderstanding of the As	sseniory lan	iguag	c prog	gramming	using R		UII
4. To develop an u	understanding of the in	terfacing te	chnia	ues w	rith 8051 N	licroco	ntroller.	
ourse Outcomes: U	pon completing this co	ourse, the st	udent	will	be able to			
1. Understand and	apply the knowledge	of addressi	ng mo	odes,	instruction	set & a	ssembl	er
directives of 808	36 to perform arithmeti	ic operation	is, sor	ting8	z String pro	ograms	using N	<b>IASM</b>
2. Design & test th	e function of Stepper	motor and 8	3255b	y inte	erfacing wi	th 8086	<b>.</b>	
3. Understand and	apply the knowledge	of addressin	ig mo	des, i	nstruction	set of 8	051 to 1	perfor
arithmetic, logi	cal & bit manipulation	n programs	using	Keil.				L
4. Able to verify the	ne operation of timer/c	ounter/UAF	RT/int	terrup	t handler i	n 8051.		
List of Experiment	nts:							
<ol> <li>Assembly Lang</li> <li>Assembly Lang</li> <li>Interfacing step</li> <li>Cycle 2: Using Ke</li> <li>Assembly Lang</li> <li>Assembly Lang</li> <li>Time delay Gen</li> <li>UART operatio</li> <li>Program and ve</li> </ol>	uage Programs to perfo per motor, ADC & DA eil IDE- (5 weeks)- guage Programs to Per- guage Programs to Per- guage Programs to per- leration Using Timers n (Serial communication rify interrupt handling	form Arithm form Rotate, AC to 8086. form Arithr form Rotate of 8051. on) in 8051. in 8051.	netic, shift , shift	Logic , Swa Logi ît, Sw	al, and Stra p and Bran cal Operation capand Bra	ions. nch Ins	truction	s.
1. Interfacing LCI	ing 1/O Devices to 805	JI (J WEEK	3)					
	) to 8051							
2. Interfacing Mat	D to 8051 rix Keyboard to 8051							
<ol> <li>Interfacing Mat</li> <li>Interfacing 8-bit</li> </ol>	D to 8051 rix Keyboard to 8051 t ADC to 8051.							
<ol> <li>Interfacing Mat</li> <li>Interfacing 8-bi</li> <li>Interfacing DAG</li> </ol>	D to 8051 rix Keyboard to 8051 t ADC to 8051. C to 8051.							
<ol> <li>Interfacing Mat</li> <li>Interfacing 8-bi</li> <li>Interfacing DAC</li> <li>List of Equipmen</li> </ol>	D to 8051 rix Keyboard to 8051 t ADC to 8051. C to 8051. t/Software (with Spe	cifications	or Ra	ange)	Required	:		
<ol> <li>Interfacing Mat</li> <li>Interfacing 8-bi</li> <li>Interfacing DAC</li> <li>List of Equipmen</li> <li>Computer Syst</li> </ol>	D to 8051 rix Keyboard to 8051 t ADC to 8051. C to 8051. ht/Software (with Spe ems(Intel) with Windo	cifications	or Ra	ange) Operat	Required	: n		
<ol> <li>Interfacing Mat</li> <li>Interfacing 8-bi</li> <li>Interfacing DAC</li> <li>List of Equipmen</li> <li>Computer Syst</li> <li>MASM 611 SC</li> </ol>	D to 8051 rix Keyboard to 8051 t ADC to 8051. C to 8051. <b>t/Software (with Spe</b> ems(Intel) with Windo oftware (Open source)	cifications	or Ra gher C	ange) )perat	Required	: n		
<ol> <li>Interfacing Mat</li> <li>Interfacing 8-bi</li> <li>Interfacing DAG</li> <li>List of Equipmen</li> <li>Computer Syst</li> <li>MASM 611 So</li> <li>Keil µVision I</li> </ol>	D to 8051 rix Keyboard to 8051 t ADC to 8051. C to 8051. <b>(t/Software (with Spe</b> ems(Intel) with Windo oftware (Open source) DE Software (Open Source)	ecifications ows 7 or hig ource)	or Ra gher C	ange) )perat	Required ing System	: n		
<ol> <li>Interfacing Mat</li> <li>Interfacing 8-bi</li> <li>Interfacing DAC</li> <li>List of Equipmen</li> <li>Computer Syst</li> <li>MASM 611 Sc</li> <li>Keil µVision I</li> <li>8086 µp kits, s</li> </ol>	D to 8051 rix Keyboard to 8051 t ADC to 8051. C to 8051. <b>it/Software (with Spe</b> ems(Intel) with Windo oftware (Open source) DE Software (Open So tepper motor interfacin	<b>cifications</b> ows 7 or hig ource) ng module,3	<b>or R</b> a gher C 8051µ	ange) )perat	<b>Required</b> ting System	: n erfacing	ŗ.	

# **EE607PC: DIGITAL SIGNAL PROCESSING LAB**

Course Code	Category	Hou	urs/W	eek	Credits	Maximum Marks			
EE(07DC	ESC	L	T	P	С	CIA	SEE	Total	
EE00/PC	ESC								
		0	0	2	1	30	70	100	
Prerequisite: Nil		l			1				
Course Objectives:									
1. To Generate and charac	terize various continuou	s and disc	rete tir	ne sig	gnals				
2. Find frequency respons	e of systems								
3. Design of digital IIR an	d FIR filters								
4. To implement Laplace	transform on a given sig	nal							
Course Outcomes: Upon c	ompleting this course, th	e student	will b	e able	e to				
1.Understand basics of MA	TLAB syntax, functions	and progr	rammi	ng					
2. Generate and characteriz	e various continuous and	l discrete t	ime si	gnals	•				
3. Analyze the spectral char $4$ Analyze the systems using	acteristics of signals using a Laplace transform and	g Fourier 7 transfo	analys	SIS.					
5 Design and simulate Digi	tal IIR and FIR filter usi	nα MATI	$\Delta \mathbf{R}$						
6 Analyse frequency response	use for the given system		AD						
List of Experiments:	ise for the given system								
1 Basic Operations on Matr	10es								
2. Generation of Various S	ignals and Sequences (Pe	eriodic and	d Aner	iodic	), such as	Unit In	nnulse.I	Init	
Step. Square. Saw tooth. Tr	iangular. Sinusoidal. Ra	mp. Sinc.	- 1 P • 1	10 410	), such us	e int in		51110	
3. Operations on Signals and	d Sequences such as Add	lition, Mul	ltiplica	ation.	Scaling,	Shifting	.Foldin	g,	
Computation of Energy and	Average Power.	,	I	,	U,		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	0,	
4. Finding the Even and Od	d parts of Signal/Sequen	ce and Re	al and	Imag	ginary part	ts of Sig	mal.		
5. Convolution for Signals a	and sequences.			Ľ	<b>J</b>	L L			
6. Verification of Linearity a	nd Time Invariance Prop	perties of a	a giver	n Con	tinuous/D	) iscretes	System.		
7. Finding the Fourier Trans	form of a given signal an	d plotting	its ma	agnitu	ide and pl	nase spe	ctrum.		
8. Waveform Synthesis usir	g Laplace Transform.	1 0		C	1	1			
9. Locating the Zeros and P	oles and plotting the Pol	e-Zero ma	aps in S	S-pla	ne and Z-l	Plane fo	or thegiv	ven	
transfer function.	1 0		1	1			U		
10. Verification of Sampling	Theorem.								
11. To find Frequency Resp	oonse of a given System	given in T	ransfe	r Fur	nction/ Di	fferentia	alequati	on	
form.	с ,	0					-		
12. Implementation of LP F	IR Filter for a given Seq	uence/Sig	mal.						
13.Implementation of HP I	R Filter for a given Seq	uence/Sig	nal						
14. Impulse Response of F	irst order and Second Or	der Syster	ms.						
st of Equipment/Software(v	with Specifications or <b>R</b>	Range) Re	quire	d:					
The Programs shall be impl	emented in Software (Us	sing MAT	LAB /	′ Lab	View / C				
Programming/ Equivalent)									

# MC610: CYBER SECURITY

Course Co	ode Ca	ategory	He	ours eek	/	Credits	its Maximum Marl		
MC610		MC	L	Т	Р	С	CIA	SEE	Tota
WICOIU		0	0	100	0	100			
Prerequisite:	NIL				1	1		1	1
Course Objec 1. To fam 2. To give 3. To stud Course Outco 1. Cyber-at	tives: iliarize varie an overvie y the defens mes: After tacks_type	ous types of cyber- w of the cyber laws sive techniques aga completion of this of s of cybercrimes	attacks and s inst these a course the	l cybo attack stude	er-cri ks ent is	$\frac{1}{able to}$	rotect	them se	lf and
ultimat	ely the entir	e Internet commun	ity from su	ch at	tacks	5 now to p 5.	loteet	uleni se	iii allu
UNIT: I		INTROI	DUCTION	TO	CYB	BER SECU	RITY		
of attacks, Tax management, etc., Comprehe	conomy of v Cyber Thre ensive Cybe	various attacks, IP ats-Cyber Warfare r Security Policy.	spoofing, c, Cyber C	Meth rime	ods ( , Cył	of defense, per terrorisi	Securi m, Cyb	ty Mode ber Espi	els, risl onage,
UNIT: II		CYBERSPACE	AND TH	E LA	W 8	CYBER	FORE	NSICS	
Introduction, C National Cybe Forensics Scie Forensics Ana Computer Fore	Cyber Secur r Security l nce, The N lysis of Em- nsics, Speci	ity Regulations, Re Policy. Introduction Need for Compute ail, Digital Forensi al Techniques for I	oles of Inte n, Historic r Forensic ics Lifecyc Forensics A	ernat al ba s, C <u>y</u> le, F Audit	ional Ickgro yber orens ing.	Law. The ound of Cy Forensics sics Investig	INDIA /ber for and Di gation,	N Cybe rensics, gital ev Challen	rspace, Digital idence, ges in
UNIT: III			CYE	BERG	CRIN	<b>1</b> E			
Mobile and W in Mobility, C Posed by Mob Attacks on M Organizational Mobile Compu	ireless Devi credit card ile Devices fobile/Cell Measures f uting Era, La	ces: Introduction, Frauds in Mobile Registry Settings Phones, Mobile for Handling Mobi	Proliferation and Wirel for Mobile Devices: le, Organiz	on of less ( e Dev Secu zation	Mob Comp vices, rity nal So	ile and Win puting Era, Authentica Implication ecurity Poli	eless E Secur ation se as for cies an	Devices, rity Cha ervice S Organiz d Measu	Trends Illenges ecurity zations ures in

UNIT: IV

# **CYBER SECURITY**

Introduction, Single-Variable Optimization, Multivariable Optimization with No Constraints, Multivariable Optimization with Equality Constraints, Multivariable Optimization with Inequality Constraints, Convex Programming Problem.

## UNIT: II

### LINEAR PROGRAMMING:

Introduction, Revised Simplex Method, Duality in Linear Programming, Decomposition Principle, Sensitivity or Postoptimality Analysis, Transportation Problem, Karmarkar's Method, Quadratic Programming

UNIT: III

## NON-LINEAR PROGRAMMING

Introduction, Unimodal Function, Unrestricted Search, Exhaustive Search, Dichotomous Search, Interval Halving Method, Fibonacci Method, Golden Section Method, Comparison of Elimination Methods, Quadratic Interpolation Method, Cubic Interpolation Method, Direct Root Methods, Rate of convergence, Design variables, Random search methods, Chrivariate methods, Powell's method, Newton's method, Marquard Method, Test function.

## Unit: IV

## **GEOMETRIC PROGRAMMING**

Introduction, Posynomial, Unconstrained Minimization Problem, Primal-Dual Relationship and Sufficiency Conditions in the Unconstrained Case, Constrained Minimization, Primal and Dual Programs in the Case of Less-Than Inequalities, Geometric Programming with Mixed Inequality Constraints, Complementary Geometric Programming, Applications of Geometric Programming.

#### Unit: V

## DYNAMIC PROGRAMMING

Introduction, Multistage Decision Processes, Concept of Sub optimization and the Principle of Optimality, Computational Procedure in Dynamic Programming, The Calculus Method of Solution, The Tabular Method of Solution, Conversion of a Final Value Problem into an Initial Value Problem, Linear Programming as a Case of Dynamic Programming, Continuous Dynamic Programming, Applications.

#### **TEXT BOOKS:**

- 1. "C B Gupta", Optimization Techniques in Operations Research, 1st Edition, I K International Publications, New Delhi, 2013.
- 2. "Singireshel S Rao", Engineering Optimizations, 4th Edition, Elsevier Butterworth, Heineman, USA, 2011.

## **REFERENCE BOOKS:**

- 1. "Jasibir Arora", Introduction to Optimum Design, 4th Edition, Academic press in an Imprint of Elsevier, USA,2016.
- "N V S Raju", Optimization Methods for Engineering, 1st edition, PHI Publications, New Delhi, 2014
- "Edwin K", "P Chang", "Stanislaw H. Zak", An Introduction to Optimization, 3rd Edition, Jhon Wiley, New York, 2013

- 1. <u>https://nptel.ac.in/courses/111/105/111105039/</u>
- 2. https://www.udemy.com/course/optimization-for-engineering-students

# EE600OE: RENEWABLE ENERGY SOURCES (Open Elective-I)

B.TECH. III YEAR II	SEMESTER								
Course Code	Category	H	ours/V	Veek	Credits	Maximum Marks			
		L	Т	Р	С	CIA	SEE	Total	
EE600OE	OEC	3	0	0	3	30	70	100	
Prerequisite: NIL	1	I				1			
Course Objectives: 1. To develop the av 2. To identify the us 3. To classify differ 4. To explain about	wareness of energy conserva e of renewable energy source ent energy storage methods. environmental effects of en	ition ces for el ergy con	ectrica version	ıl pow	er generatio	on			
Course Outcomes: Upo 1. Understand the pu 2. Assess the cost of 3. Design suitable pu 4. Analyze the issue 5. Discuss the variou	on completing this course, the inciples of wind power and f generation for conventionar ower controller for wind and s involved in the integration us energy storage methods.	ne student solar pho al and rer d solar ap of renew	t will otovol newabl oplicat wable	be able taic po le ener tions energy	e to ower genera gy plants v sources to	tion, fue the grid	el cells.		
UNIT: I	ECONC	DMICS H	RENE	WAB	LE ENER	GY			
Options-Modern Elect Wind Power Plants Purpose of the Energy Multiple-Blade Turbin in Wind Power Energy Small Generating System	ronic Controls of Power S Appropriate Location gy Generated - General nes Drag Turbines -Lift y Analysis of tems.	Systems. -Evalua Classifi	ation ication bines	of V n of -Gene	Wind Inte Wind Tur erators and	ensity - bines-R I Speed	-Topogr otor Tu l Contro	aphy - irbines- ol used	
UNIT: II	PHOTOVOLTAI	C POW	ER P	LANT	'S AND FU	JEL CE	LLS		
Solar Energy-Generati Characteristic on Te Parameters for Photovo Economical Analysis of The Fuel Cell-Low a Constructional Feature Systems and Related Equivalent Practical Determination	on of Electricity by H emperature-Solar cell ltaic Panels- Photovoltaic f Solar Energy. and High Temperature H es of Proton Exchange- Precautions-Advantages on of the Equivalent Mode	Photovol Output Systems Fuel Ce Membra and I el Param	taic Cha s-App lls-Cc ane H Disady	Effect racter licatio ommen Fuel vantag	t -Depend istics-Equi ons of Pho rcial and Cells –R ges of Hy	lence o valent tovoltaio Manufa eformer Fuel C drogen	of a P Model c Solar I acturing rs-Electr ells-Fue as Fuel.	V Cell s and Energy- Issues ro-lyzer t Cell	
UNIT: III	ÎN	DUCTIO	DN G	ÊNÊR	ATORS				
Principles of Operation Self Excited Induct Description of the S and Voltage Control Economical Aspects.	on-Representation of Stea ion Generator-Magnetiz elf-Excitation Process-In -	ndy-State ing Cu terconne	e Ope rves ected	eration and and	-Power a Self-Exc Stand-alor	nd Loss itation ne ope	ses Ger Mathe ration	nerated- matical -Speed	

UNIT: IV

## **STORAGE SYSTEMS**

Energy Storage Parameters-Lead–Acid Batteries-Ultra Capacitors-Flywheels –Superconducting Magnetic Storage System-Pumped Hydroelectric Energy Storage - Compressed Air Energy Storage - Storage Heat -Energy Storage as an Economic Resource.

UNIT: V

INTEGRATION OF ALTERNATIVE SOURCES OF ENERGY:

Principles of Power Injection-Instantaneous Active and Reactive Power Control Approach Integration of Multiple Renewable Energy Sources-Islanding and Interconnection Control-DG

Control and Power Injection. Interconnection of Alternative Energy Sources with the Grid:

Interconnection Technologies - Standards and Codes for Interconnection - Interconnection

Considerations - Interconnection Examples for Alternative

Energy Sources.

## **TEXT BOOKS:**

- 1. "John Twidell", "Tony Weir", Renewable Energy Resources, Routledge, 2015
- 2. "Mehmet Kanoglu","YunusA.Cengel","JohnM.Cimbala", Fundamentals and Applications of Renewable Energy,McGraw-Hill Education,2020

#### **REFERENCE BOOKS:**

- 1. "S.C. Bhatia", "R.K. Gupta", Renewable Energy, Woodhead, 2018
- 2. "Mehmet Kanoglu", "Yunus A. Cengel", "John M. Cimbala" ,Fundamentals and Applications of Renewable Energy | Indian Edition -2020
- 3. "Anand Tembulkar", "S.P. Meher, Kataria", Non-Conventional Energy Sources, 2013

- 1. https://www.eia.gov/energyexplained/renewable-sources/
- 2. <u>https://nptel.ac.in/courses/103/103/103103206/</u>

#### EE601OE: RELIABILITY ENGINEERING

#### **B.TECH. III YEAR II SEMESTER** Hours/Week Credits **Maximum Marks Course Code** Category Р С L Т CIA SEE Total **EE601OE** OEC 3 0 0 3 30 70 100 Prerequisite: Laplace Transforms, Numerical Methods and Complex variables (MA401BS) **Course Objectives:** 1. To understand the basic concepts of reliability, various models of reliability 2. To analyze reliability of various systems 3. To discuss the concept of Discrete Markov Chains 4. To explain the techniques of frequency and duration for reliability evaluation of repairable Systems **Course Outcomes:** Upon completing this course, the student will be able to 1. Discuss various systems of reliability networks 2. Evaluate the reliability of simple and complex systems 3. Estimate the limiting state probabilities of repairable systems 4. Apply various distribution functions for reliability evaluation. 5. Apply various mathematical models for evaluating reliability of irreparable systems UNIT: I **BASIC PROBABILITY THEORY** Elements of probability, probability distributions, Random variables, Density and Distribution functions-Mathematical expected - variance and standard deviation Binomial Distribution: Concepts, properties, engineering applications. UNIT: II NETWORK MODELING AND EVALUATION OF SIMPLE SYSTEMS Basic concepts- Evaluation of network Reliability / Unreliability - Series systems, Parallel systems - Series-Parallel systems- Partially redundant systems- Examples. Conditional probability method- tie set, Cut-set approach- Event tree and reduced event tree methods Relationships between tie and cut-sets- Examples UNIT: III PROBABILITY DISTRIBUTIONS IN RELIABILITY EVALUATION Distribution concepts, Terminology of distributions, General reliability functions, Evaluation of the reliability functions, shape of reliability functions -Poisson distribution - normal distribution, exponential distribution. Weibull distribution. Network Reliability Evaluation Using Probability Distributions: Reliability Evaluation of Series systems, Parallel systems - Partially redundant systems- determination of reliability measure- MTTF for series and parallel systems - Examples. **UNIT: IV DISCRETE MARKOV CHAINS** Basic concepts- Stochastic transitional probability matrix- time dependent probability evaluation- Limiting State Probability evaluation- Absorbing states - Application. Continuous Markov Processes: Modeling concepts- State space diagrams- Unreliability evaluation of single and two component repairable systems UNIT: V **FREQUENCY AND DURATION TECHNIQUES** Frequency and duration concepts, application to multi state problems, Frequency balance approach. Approximate System Reliability Evaluation: Series systems – Parallel systems- Network reduction techniques- Cut set approach- Common mode failures modeling and evaluation techniques- Examples.

(Open Elective-I)

#### **TEXT BOOKS:**

- 1. "Roy Billinton", "Ronald N Allan", Reliability Evaluation of Engineering Systems, Plenum Press 2013.
- 2. "E. Balagurusamy", Reliability Engineering by Tata McGraw-Hill Publishing Company Limited 2017

## **REFERENCE BOOKS:**

- 1. "Alessandro Birolini", Reliability Engineering: Theory and Practice Springer Publications-2018
- 2. "Charles Ebeling", An Introduction to Reliability and Maintainability Engineering, TMH Publications 2017.
- 3. "Elsayed A", Reliability Engineering, Third Edition, John Wiley and Sons Ltd 2021

- 1. <u>https://nptel.ac.in/courses/111/101/111101004/</u>
- 2. <u>https://nptel.ac.in/courses/105/108/105108128/</u>